

Sync vs Async Communication

Request and wait for a response
Fire and forget with messages/events

@lorenz/23

Synchronous



Request-response model

Send a request and wait for some response
Example: API requests

Asynchronous



Fire and forget model

Send message/event and forget
Example: Event Driven Architecture

Advanced Async Communication

Zhe Xu

A decorative graphic element consisting of a horizontal light blue bar with a rounded right end, overlaid on a red circular gradient background.

Advanced Async Communication:

Recent Advances in the Message Passing Interface Claudia Blaas-Schenner, Christoph Niethammer, Tobias Haas, 2024-09-24 This volume LNCS constitutes the refereed proceedings of 31st European MPI Users Group Meeting EuroMPI 2024 held in Perth WA Australia during September 25 27 2024 The 8 full papers presented here were carefully reviewed and selected from 19 submissions These papers have been categorized under the following topical sections Compile time Correctness checks and optimization Limitations and Extensions for GPGPUs in MPI Improvements for MPI and MPI Ecosystem

Low-Power Processors and Systems on Chips Christian Piguet, 2018-10-03 The power consumption of microprocessors is one of the most important challenges of high performance chips and portable devices In chapters drawn from Piguet's recently published *Low Power Electronics Design* this volume addresses the design of low power microprocessors in deep submicron technologies It provides a focused reference for specialists involved in systems on chips from low power microprocessors to DSP cores reconfigurable processors memories ad hoc networks and embedded software *Low Power Processors and Systems on Chips* is organized into three broad sections for convenient access The first section examines the design of digital signal processors for embedded applications and techniques for reducing dynamic and static power at the electrical and system levels The second part describes several aspects of low power systems on chips including hardware and embedded software aspects efficient data storage networks on chips and applications such as routing strategies in wireless RF sensing and actuating devices The final section discusses embedded software issues including details on compilers retargetable compilers and coverification tools Providing detailed examinations contributed by leading experts *Low Power Processors and Systems on Chips* supplies authoritative information on how to maintain high performance while lowering power consumption in modern processors and SoCs It is a must read for anyone designing modern computers or embedded systems

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Communications and Networking for the IBM PC and Compatibles Larry E. Jordan,Bruce Churchill,1990 Introduces the personal computer novice to the spectrum of data communications and networking capabilities of the IBM PC and Personal System 2 families Describes both hardware and software facets No bibliography Annotation copyrighted by Book News Inc Portland OR

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