

David A. Hodges  
Horace G. Jackson  
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Third Edition



**Analysis and Design of  
Digital Integrated Circuits**

*In Deep Submicron Technology*

# Submicron Integrated Circuits

**Raminderpal Singh**



## **Submicron Integrated Circuits:**

Submicron Integrated Circuits R. K. Watts, 1989-05-29 Comprehensive treatment of integrated circuits in the submicron range First chapters cover basic devices used in circuits such as silicon MOSFET s bipolar transistors GaAs MESFET s and HEMT s There is also a chapter on the relatively new GaAs resonant tunneling diode and transistor Middle chapters address architecture and circuit wiring Last chapters discuss rapid thermal processing and lithography Chip Design for Submicron VLSI John Paul Uyemura, 2006 This book teaches the principles of physical design layout and simulation of CMOS integrated circuits It is written around a very powerful CAD program called Microwind that is available on the accompanying CD ROM Featuring a friendly interface Microwind is both educational and useful for designing CMOS chips

Deep-Submicron CMOS ICS Harry Veendrick, 2000-12-31 Nowadays CMOS technologies account for almost 90% of all integrated circuits ICs This book provides an essential introduction to CMOS ICs The contents of this book are based upon a previous publication entitled MOS ICs which was published in Dutch and English by Delta Press Amerongen The Netherlands 1990 and VCH Weinheim Germany 1992 respectively This book contains state of the art material but also focuses on aspects of scaling up to and beyond 0.1  $\mu\text{m}$  CMOS technologies and designs It clearly describes the fundamental CMOS operating principles and presents substantial insight into various aspects of design implementation and application In contrast to other works on this topic the book explores all associated disciplines of deep submicron CMOS ICs including physics design technology and packaging low power design and signal integrity The text is based upon in house Philips courseware which to date has been completed by more than 1500 engineers Carefully structured and enriched by hundreds of figures photographs and in depth exercises the book is well suited for the purpose of self study **Analysis and Design of Digital Integrated Circuits** David A. Hodges, Horace G. Jackson, Resve A. Saleh, 2005 *Micron and Submicron Integrated Circuit Metrology* Kevin M. Monahan, 1985 **Stochastic Process Variation in Deep-Submicron CMOS** Amir Zjajo, 2013-11-19 One of the most notable features of nanometer scale CMOS technology is the increasing magnitude of variability of the key device parameters affecting performance of integrated circuits The growth of variability can be attributed to multiple factors including the difficulty of manufacturing control the emergence of new systematic variation generating mechanisms and most importantly the increase in atomic scale randomness where device operation must be described as a stochastic process In addition to wide sense stationary stochastic device variability and temperature variation existence of non stationary stochastic electrical noise associated with fundamental processes in integrated circuit devices represents an elementary limit on the performance of electronic circuits In an attempt to address these issues Stochastic Process Variation in Deep Submicron CMOS Circuits and Algorithms offers unique combination of mathematical treatment of random process variation electrical noise and temperature and necessary circuit realizations for on chip monitoring and performance calibration The associated problems are addressed at various abstraction levels i.e. circuit level architecture level and system level It

therefore provides a broad view on the various solutions that have to be used and their possible combination in very effective complementary techniques for both analog mixed signal and digital circuits The feasibility of the described algorithms and built in circuitry has been verified by measurements from the silicon prototypes fabricated in standard 90 nm and 65 nm CMOS technology

**Baseband Analog Circuits in Deep-submicron CMOS Technologies Targeted for Mobile Multimedia** Vijayakumar Dhanasekaran,2010 Three main analog circuit building blocks that are important for a mixed signal system are investigated in this work New building blocks with emphasis on power efficiency and compatibility with deep submicron technology are proposed and experimental results from prototype integrated circuits are presented Firstly a 1.1GHz 5th order active LC Butterworth wideband equalizer that controls inter symbol interference and provides anti alias filtering for the subsequent analog to digital converter is presented The equalizer design is based on a new series LC resonator biquad whose power efficiency is analytically shown to be better than a conventional Gm C biquad A prototype equalizer is fabricated in a standard 0.18  $\mu$ m CMOS technology It is experimentally verified to achieve an equalization gain programmable over a 0.23dB range 47dB SNR and 48dB IM3 while consuming 72mW of power This corresponds to more than 7 times improvement in power efficiency over conventional Gm C equalizers Secondly a load capacitance aware compensation for 3 stage amplifiers is presented A class AB 16W headphone driver designed using this scheme in 130nm technology is experimentally shown to handle 1pF to 22nF capacitive load while consuming as low as 1.2mW of quiescent power It can deliver a maximum RMS power of 20mW to the load with 84.8dB THD and 92dB peak SNR and it occupies a small area of 0.1mm<sup>2</sup> The power consumption is reduced by about 10 times compared to drivers that can support such a wide range of capacitive loads Thirdly a novel approach to design of ADC in deep submicron technology is described The presented technique enables the usage of time to digital converter TDC in a delta sigma modulator in a manner that takes advantage of its high timing precision while noise shaping the error due to its limited time resolution A prototype ADC designed based on this deep submicron technology friendly architecture was fabricated in a 65nm digital CMOS technology The ADC is experimentally shown to achieve 68dB dynamic range in 20MHz signal bandwidth while consuming 10.5mW of power It is projected to reduce power and improve speed with technology scaling

*Space Microelectronics Volume 2: Integrated Circuit Design for Space Applications* Anatoly Belous,Vitali Saladukha, ,Siarhei Shvedau,2017-07-31 This invaluable second volume of a two volume set is filled with details about the integrated circuit design for space applications Various considerations for the selection and application of electronic components for designing spacecraft are discussed The basic constructions of submicron transistors and schottky diodes during the technological process of production are explored This book provides details on the energy consumption minimization methods for microelectronic devices Specific topics include Features and physical mechanisms of the effect of space radiation on all the main classes of microcircuits including peculiarities of radiation impact on submicron integrated circuits Special design technology and schematic methods of

increasing the resistance to various types of space radiation Recommendations for choosing research equipment and methods for irradiating various samples Microcircuit designers on the composition of test elements for the study of the effect of radiation Microprocessors circuit boards logic microcircuits digital analog digital analog microcircuits manufactured in various technologies bipolar CMOS BiCMOS SOI Problems involved with designing high speed microelectronic devices and systems based on SOS and SOI structures System on chip and system in package and methods for rejection of silicon microcircuits with hidden defects during mass production

**Analysis and Design of Digital Integrated Circuits** David A. Hodges, Horace G. Jackson, 1983 This is a state of the art treatment of the circuit design of digital integrated circuits It includes coverage of the basic concepts of static characteristics voltage transfer characteristics noise margins fanout power dissipation and dynamic characteristics propagation delay times and the interrelationships among these parameters The authors are regarded as leading authorities in integrated circuits and MOS technology

**Federal Trade Commission Decisions** United States. Federal Trade Commission, 1999

**Signal Integrity Effects in Custom IC and ASIC Designs** Raminderpal Singh, 2001-12-12 offers a tutorial guide to IC designers who want to move to the next level of chip design by unlocking the secrets of signal integrity Jake Buurma Senior Vice President Worldwide Research Development Cadence Design Systems Inc Covers signal integrity effects in high performance Radio Frequency RF IC Brings together research papers from the past few years that address the broad range of issues faced by IC designers and CAD managers now and in the future A Wiley IEEE Press publication

Federal Register ,1997-05-14

Micron and Submicron Integrated Circuit Metrology ,1985

**Electronic Materials Handbook** ,1989-11-01 Volume 1 Packaging is an authoritative reference source of practical information for the design or process engineer who must make informed day to day decisions about the materials and processes of microelectronic packaging Its 117 articles offer the collective knowledge wisdom and judgement of 407 microelectronics packaging experts authors co authors and reviewers representing 192 companies universities laboratories and other organizations This is the inaugural volume of ASMAs all new ElectronicMaterials Handbook series designed to be the Metals Handbook of electronics technology In over 65 years of publishing the Metals Handbook ASM has developed a unique editorial method of compiling large technical reference books ASMAs access to leading materials technology experts enables to organize these books on an industry consensus basis Behind every article Is an author who is a top expert in its specific subject area This multi author approach ensures the best most timely information throughout Individually selected panels of 5 and 6 peers review each article for technical accuracy generic point of view and completeness Volumes in the Electronic Materials Handbook series are multidisciplinary to reflect industry practice applied in integrating multiple technology disciplines necessary to any program in advanced electronics Volume 1 Packaging focusing on the middle level of the electronics technology size spectrum offers the greatest practical value to the largest and broadest group of users Future volumes in the series will address topics on larger integrated electronic assemblies and smaller semiconductor materials and

devices size levels      1995 4th International Conference on Solid-State and Integrated Circuit Technology International Conference on Solid-State and Integrated Circuit Technology, IEEE Electron Devices Society, 1995      **Advanced**

**Interconnects and Contact Materials and Processes for Future Integrated Circuits: Volume 514** S. P.

Murarka, 1998-11-02 Comprising the proceedings of an MRS symposium held in April of 1998 contributions in this volume are divided into ten sections interconnection frontiers aluminum interconnects cobalt and other silicides titanium silicide MOSFET source drain and interconnect engineering copper interconnects and barriers a poster session on advanced interconnects and contacts contacts to compound semiconductor devices novel interconnect materials and schemes and diffusion barriers Annotation copyrighted by Book News Inc Portland OR      **Crosstalk Noise in Deep Submicron**

**Integrated Circuit Design** Murat R. Becer, 2003      **Submicron CMOS Programmable Analog Floating-gate Circuits**

**and Arrays Using DC-DC Converters** Mark S. Hooper, 2005 A relatively new area of analog integrated circuits is emerging which is likely to have an impact on the signal processing area analog floating gate circuits Analog floating gate circuits have the potential to deliver more sophisticated signal processing at less power in a smaller space This is the result of a novel application of digital memory technology the floating gate MOSFET that is used as an analog memory and computational device Critical to the success of analog floating gate circuits is on chip programming After investigating integrated schemes for DC DC converters to generate the necessary voltages on chip this research focuses on charge pumps that are integrated into the programming structure of floating gate circuits The impact of this research is far reaching since programmability is an indispensable feature of analog floating gate circuits This research lays the foundation for meeting the requirement of on chip programming Charge pumps will eliminate the need for high voltages to be externally supplied or regulated for analog floating gate circuits To the design engineer the utilization of floating gate circuits will look identical to their non floating gate counterparts in terms of the value and number of supply voltages In addition the integration of on chip DC DC converters will reduce pin count reduce board space for the implementation of the chip and facilitate distributed on chip power supplies for mixed signal integrated circuits      **Integrated Circuit Fabrication Technology** David J. Elliott, 1982

*13th Symposium on Integrated Circuits and Systems Design* Ricardo Augusto da Luz Reis, Jose Carlos Monteiro, Wilhelmus Van Noije, 2000 These papers are taken from 13th Brazilian Symposium on Integrated Circuit Design SBCCI 2000 They address issues such as microarchitectures architecture logic design analogue design high level synthesis digital design physical modelling reconfigurable hardware and more

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web this external business environment is composed of numerous outside organizations and forces that we can group into seven key subenvironments as exhibit 1 4 illustrates economic political and legal demographic social competitive global and technological