

# Synthesis of Switching Circuits

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# Synthesis Of Switching Circuits

**J. Klir, L. Seidl**



## Synthesis Of Switching Circuits:

**Synthesis of Switching Circuits** George J. Klir, Lev K. Seidl, 1968      **Synthesis of Switching Circuits** J. Klir, L. Seidl, 1964-05-01      **The Synthesis of Sequential Switching Circuits** David A. Huffman, Massachusetts Institute of Technology. Research Laboratory of Electronics, 1953      **A Method for the Analysis and Synthesis of Sequential Switching Circuits** Robert Vaughn Morse, 1956      **Synthesis of Switching Circuits [by] Jiří Klír [and] Lev K. Seidl** Jiří Klír, 1969      Algebraic Synthesis of Switching Circuits Shoukat Ali Lakhani, 1969      **The Synthesis of Relay Switching Circuits** Vadim N. Roginskii, 1959      The Synthesis of Relay Switching Circuits Vadim Nikolaevich Roginskii, 1963      **An Algorithm for the Synthesis of Large Sequential Switching Circuits** John Charles Elsey, 1963

With the development and widespread use of large digital computers which are switching circuits there has been an increasing interest in switching circuit theory. Much effort has been spent in designing or synthesizing switching circuits but unfortunately many of the procedures now in use are more of an art than a science in that much insight and experience are usually required on the part of the designer. Some systematic design methods have been introduced but these are only applicable to rather small circuits. This thesis develops and presents an algorithm for synthesizing asynchronous sequential switching circuits. The algorithm has certain properties which make it useful and applicable to large switching circuits such as the control unit of a digital computer. The steps in the algorithm are systematic and simple permitting a solution to be obtained in a relatively short amount of time. The resulting design is not optimum in that the circuit has the minimum number of states or minimum amount of logic.

Author      *Synthesis of Modular Switching Circuits* Kenneth Herbert O'Keefe, 1963      *THREE PROBLEMS IN THE SYNTHESIS OF SWITCHING CIRCUITS*. Barrett Hazeltine, 1962      The Synthesis of Relay Switching Circuits. Translated by W. Chrzczoneowicz [sic]. Vadim Nikolaevich Roginskii, 1963      *The Synthesis of Cascade Switching Circuits* Roger Eli Levien, RAND CORP SANTA MONICA CALIF., Rand Corporation, 1962

The problem is considered of synthesizing switching circuits that compute a given completely specified switching function and a new point of view is adopted in which switching circuits are considered to be realizations of algorithms rather than interpretations of truth functional formulae. A restricted class of algorithms called the cascade algorithms is identified. It is suggested that tree and collapsed tree iterative multiple iterative sequential and cascaded sequential switching circuits may be viewed as realizations of cascade algorithms. All of these circuits are called therefore cascade circuits. Three basic techniques functional decomposition, merging and skipping which permit the synthesis of efficient cascade algorithms to compute a given function are described. It is then shown how these techniques may be applied so as to synthesize a collapsed tree iterative multiple iterative sequential or cascaded sequential switching circuit that computes a given switching function. Examples are drawn from the technology of current steering devices such as relays and cryotrons but the method is applicable to other technologies. The procedures appear to be well adapted to execution on contemporary digital computers.

Author      The Synthesis Of Relay Switching

Circuits V.N. Roginskii, *The Synthesis of Relay Switching Circuits ... With 108 Illustrations and 25 Tables. Translated by W. Chrzczonowicz, Etc* Vadim Nikolaevich ROGINSKY, W. CHRZCZONOWICZ, 1963     **Synthesis of Asynchronous Sequential Switching Circuits** Chung Jen Tan, 1969     **Synthesis of switching circuits using negative gates** K. K. Chakrabarti, 1972     **Analysis and Synthesis of Sequential Switching Circuits** Zvi Kohavi, 1963     *Analysis and Synthesis of Sequential Switching Circuits* Zvi Kohavi, 1963     Synthesis and Analysis of Relay-switching Circuits Using Characteristic Functions A. G. Lunts, 1950

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