



# SystemC: A Comprehensive Guide To System-Level Modeling And Simulation

# System Design With Systemc

**Arturo Cuomo**



## **System Design With Systemc:**

**System Design with SystemCTM** Thorsten Grötter, Stan Liao, Grant Martin, Stuart Swan, 2007-05-08 I am honored and delighted to write the foreword to this very first book about SystemC It is now an excellent time to summarize what SystemC really is and what it can be used for The main message in the area of design in the 2001 International Technology Roadmap for Semiconductors ITRS is that cost of design is the greatest threat to the continuation of the semiconductor roadmap This recent revision of the ITRS describes the major productivity improvements of the last few years as small block reuse large block reuse and IC implementation tools In order to continue to reduce design cost the required future solutions will be intelligent test benches and embedded system level methodology As the new system level specification and design language SystemC directly contributes to these two solutions These will have the biggest impact on future design technology and will reduce system implementation cost It took SystemC less than two years to emerge as the leader among the many new and well discussed system level design languages In my opinion this is due to the fact that SystemC adopted object oriented system level design the most promising method already applied by the majority of firms during the last couple of years Even before the introduction of SystemC many system designers have attempted to develop executable specifications in C These executable functional specifications are then refined to the well known transaction level to model the communication of system level processes

**SystemC: From the Ground Up** David C. Black, Jack Donovan, 2004-05-31 SystemC provides a robust set of extensions to C that enables rapid development of complex hardware software systems This book focuses on the practical uses of the language for modeling real systems The wealth of examples and downloadable code methodically guide the reader through the finer points of the SystemC language This work provides A step by step build up of syntax NEW features of SystemC 2.1 Code examples for each concept Many resource references Coding styles and guidelines Over 52 downloadable code examples over 8 000 lines Exercises throughout the book How SystemC fits into the system design methodology Why features are as they are Well known consultants in the EDA industry both David Black and Jack Donovan have been involved in the adoption and teaching of new technologies and methodologies for a combined total of 42 years Recently they jointly founded a consultancy Eklektic Ally focused on helping companies adopt SystemC methodologies System Level Design with SystemC Daghan S. Altas, 2000 In this thesis we provide a system level design methodology aimed for the design of embedded systems Our methodology is geared towards the concurrent design of hardware components and software agents Our choice of tool language pair is CoWare N2C design tool and SystemC specification language Our design flow is completed with a high performance hardware synthesis methodology relying behavioral synthesis A case study the design of an ATM switch is carried out to illustrate the proposed ideas **VLSI Circuits and Systems**, 2005 Design and Test Technology for Dependable Systems-on-Chip Ubar, Raimund, Raik, Jaan, Vierhaus, Heinrich Theodor, 2010-12-31 This book covers aspects of system design and efficient modelling and also introduces various fault models and fault mechanisms

associated with digital circuits integrated into System on Chip SoC Multi Processor System on Chip MPSoC or Network on Chip NoC

*System-Level Design and Modeling* Zainalabedin Navabi,2016-01-03 This book discusses principles methodologies and tools used for a modern hardware design process Design flows and hardware languages needed for each stage of the design process are discussed including C for description of gate and Boolean level logic circuits SystemC for RTL descriptions and TLM 2.0 for system level design and description Following the coverage of languages and techniques for system level design the book includes discussion of several system level architectures that can be described using C SystemC and TLM 2.0

**Computer Supported Cooperative Work in Design I** Weiming Shen,Zongkai Lin,Jean-Paul A. Barthès,Tangqiu Li,2005-11-03 The design of complex artifacts and systems requires the cooperation of multidisciplinary design teams using multiple commercial and non commercial engineering tools such as CAD tools modeling simulation and optimization software engineering databases and knowledge based systems Individuals or individual groups of multidisciplinary design teams usually work in parallel and separately with various engineering tools which are located on different sites often for quite a long time At any moment individual members may be working on different versions of a design or viewing the design from various perspectives at different levels of detail In order to meet these requirements it is necessary to have effective and efficient collaborative design environments These environments should not only automate individual tasks in the manner of traditional computer aided engineering tools but also enable individual members to share information collaborate and coordinate their activities within the context of a design project CSCW computer supported cooperative work in design is concerned with the development of such environments

Quality-Driven SystemC Design Daniel Große,Rolf Drechsler,2009-12-02 A quality driven design and verification flow for digital systems is developed and presented in Quality Driven SystemC Design Two major enhancements characterize the new flow First dedicated verification techniques are integrated which target the different levels of abstraction Second each verification technique is complemented by an approach to measure the achieved verification quality The new flow distinguishes three levels of abstraction namely system level top level and block level and can be incorporated in existing approaches After reviewing the preliminary concepts in the following chapters the three levels for modeling and verification are considered in detail At each level the verification quality is measured In summary following the new design and verification flow a high overall quality results

**SystemC: From the Ground Up, Second Edition** David C. Black,Jack Donovan,Bill Bunton,Anna Keist,2009-12-18 SystemC provides a robust set of extensions to the C language that enables rapid development of complex models of hardware and software systems The authors focus on practical use of the language for modeling real systems showing A step by step build up of syntax Code examples for each concept Updates to reflect the SystemC standard IEEE 1666 Why features are as they are Many resource references How SystemC fits into an ESL methodology This new edition of an industry best seller is updated to reflect the standardization of SystemC as IEEE 1666 and other improvements that

reflect feedback from readers of the first edition The wide ranging feedback also include suggestions from editors of the Japanese and Korean language translations professors and students and computer engineers from a broad industrial and geographical spectrum all who have successfully used the first edition New chapters have been added on the SystemC Verification Library and the Transaction Level Modeling and proposed changes to the current SystemC standard David Black and Jack Donovan well known consultants in the EDA industry have teamed with Bill Bunton and Anna Keist experienced SystemC modeling engineers to write the second edition of this highly popular classic As a team the authors bring over 100 years of ASIC and system design experience together to make a very readable introduction to SystemC

*Software Engineering and Formal Methods* Dimitra Giannakopoulou, Gwen Salaün, 2014-08-04 This book constitutes the refereed proceedings of the 12th International Conference on Software Engineering and Formal Methods SEFM 2014 held in Grenoble France in September 2014 The 23 full papers presented together with 3 invited and 6 tool papers were carefully reviewed and selected from 106 submissions They are organized in topical section on program verification testing component based systems real time and embedded systems model checking and automata learning program correctness and adaptive and multi agent systems

Nanotechnology: Concepts, Methodologies, Tools, and Applications Management Association, Information Resources, 2014-02-28 Over the past few decades devices and technologies have been significantly miniaturized from one generation to the next providing far more potential in a much smaller package The smallest of these recently developed tools are miniscule enough to be invisible to the naked eye Nanotechnology Concepts Methodologies Tools and Applications describes some of the latest advances in microscopic technologies in fields as diverse as biochemistry materials science medicine and electronics Through its investigation of theories applications and new developments in the nanotechnology field this impressive reference source will serve as a valuable tool for researchers engineers academics and students alike

*IEEE International High-Level Design Validation and Test Workshop* ,2002 Integrated Circuit and System Design ,2005 Real-Time Systems Symposium American Society of Mechanical Engineers, 2004 A System-level Platform-based Multi-core System-on-chip Simulation Framework with Retargetable Processing Element Models Jinwen Xi, 2007 IEEE Workshop on Signal Processing Systems ,2001 *14th Symposium on Integrated Circuits and Systems Design* Sociedade Brasileira de Computação, 2001 Annotation Papers from a September 2001 symposium report on recent advances in areas of integrated circuits and systems design including embedded systems rapid prototyping formal methods codesign CAD and test analog digital and physical design and low power and low voltage Specific topics include communication architectures for system on chip using the CAN protocol and reconfigurable computing technology for Web based smart house automation and optimizing BDD based verification analyzing variable dependencies Other subjects include interconnection length estimation at logic level an environment to aid the synthesis of threephase analogue waveform using AHDL and extending sequencing graphs for reconfigurable applications modeling This work lacks a subject index c Book

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