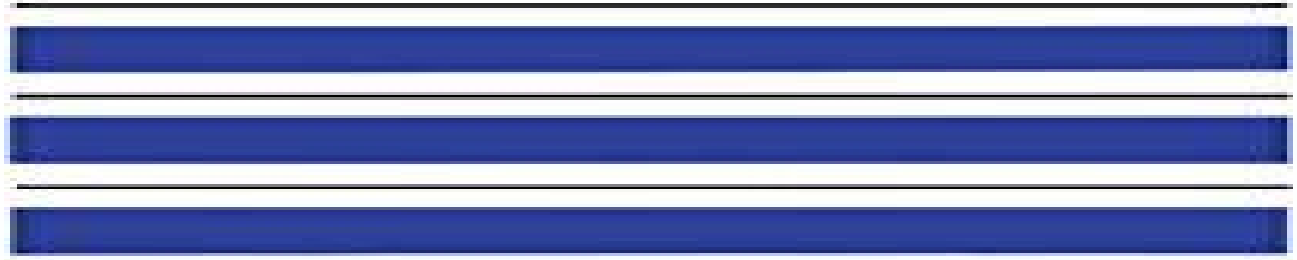
A VLSI Architecture for Concurrent Data Structures

William J. Dally



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Clemens Wendtner

Vlsi Architecture For Concurrent Data Structures:

A VLSI Architecture for Concurrent Data Structures J. W. Dally,2012-12-06 Concurrent data structures simplify the development of concurrent programs by encapsulating commonly used mechanisms for synchronization and commu nication into data structures This thesis develops a notation for describing concurrent data structures presents examples of concurrent data structures and describes an architecture to support concurrent data structures Concurrent Smalltalk CST a derivative of Smalltalk 80 with extensions for concurrency is developed to describe concurrent data structures CST allows the programmer to specify objects that are distributed over the nodes of a concurrent computer These distributed objects have many constituent objects and thus can process many messages simultaneously They are the foundation upon which concurrent data structures are built The balanced cube is a concurrent data structure for ordered sets The set is distributed by a balanced recursive partition that maps to the subcubes of a binary 7lrcube using a Gray code A search algorithm VW search based on the distance properties of the Gray code searches a balanced cube in O log N time Because it does not have the root bottleneck that limits all tree based data structures to 0 1 concurrency the balanced cube achieves 0C N con currency Considering graphs as concurrent data structures graph algorithms are pre sented for the shortest path problem the max flow problem and graph parti tioning These algorithms introduce new synchronization techniques to achieve better performance than existing algorithms **A VLSI Architecture for Concurrent Data Structures** William J. Dally,1987

Parallel Supercomputing in MIMD Architectures R.Michael Hord,2018-02-01 Parallel Supercomputing in MIMD Architectures is devoted to supercomputing on a wide variety of Multiple Instruction Multiple Data MIMD class parallel machines This book describes architectural concepts commercial and research hardware implementations major programming concepts algorithmic methods representative applications and benefits and drawbacks Commercial machines described include Connection Machine 5 NCUBE Butterfly Meiko Intel iPSC iPSC 2 and iWarp DSP3 Multimax Sequent and Teradata Research machines covered include the J Machine PAX Concert and ASP Operating systems languages translating sequential programs to parallel and semiautomatic parallelizing are aspects of MIMD software addressed in Parallel Supercomputing in MIMD Architectures MIMD issues such as scalability partitioning processor utilization and heterogenous networks are discussed as well This book is packed with important information and richly illustrated with diagrams and tables Parallel Supercomputing in MIMD Architectures is an essential reference for computer professionals program managers applications system designers scientists engineers and students in the computer sciences **Parle ’91 Parallel Architectures and Languages Europe** Emile H.L. Aarts,Jan van Leeuwen,Martin Rem,2013-11-11 The innovative progress in the development oflarge and small scale parallel computing systems and their increasing availability have caused a sharp rise in interest in the scientific principles that underlie parallel computation and parallel programming The biannual Parallel Architectures and Languages Europe PARLE conferences aim at presenting current research material on all aspects of the

theory design and application of parallel computing systems and parallel processing At the same time the goal of the PARLE conferences is to provide a forum for researchers and practitioners to ex change ideas on recent developments and trends in the field of parallel com puting and parallel programming The first wo conferences PARLE 87 and PARLE 89 have succeeded in meeting this goal and made PARLE a conference that is recognized worldwide in the field of parallel computation PARLE 91 again offers a wealth of high quality research material for the benefit of the scientific community Compared to its predecessors the scope of PARLE 91 has been broadened so as to cover the area of parallel algo rithms and complexity in addition to the central themes of parallel archi tectures and languages The proceedings of the PARLE 91 conference contain the text of all con tributed papers that were selected for the programme and of the invited papers by leading experts in the field **Serial-Data Computation** Stewart G. Smith,Peter B. Denyer,2012-12-06 This book is concerned with advances in serial data computa tional architectures and the CAD tools for their implementation in silicon The bit serial tradition at Edinburgh University EU stretches back some 6 years to the conception of the FIRST silicon compiler FIRST owes much of its inspiration to Dick Lyon then at Xerox P ARC who proposed a structured design methodology for construction of signal processing systems from bit serial building blocks Based on an nMOS cell library FIRST automates much of Lyon s physical design process More recently we began to feel that FIRST should be able to exploit more modern technologies Before this could be achieved we were faced with a massive manual re design task i e the porting of FIRST cell library to a new technology As it was to avoid such tasks that FIRST was conceived in the first place we decided to move the level of user specification much nearer to the silicon level while still hiding details of transistor circuit design place and route etc from the user and by so doing enable the specification of more functionally powerful libraries in technology free form The results of this work are in evidence as advances in serial data design techniques and the SECOND silicon compiler introduced later in this book These achievements could not have been accomplished without help from various sources We take this opportunity to thank Profs **Steady-State Methods for Simulating Analog and Microwave Circuits** Kenneth S. Kundert,Jacob K. White,Alberto L. Sangiovanni-Vincentelli,2013-03-09 The motivation for starting the work described in this book was the interest that Hewlett Packard s microwave circuit designers had in simulation techniques that could tackle the problem of finding steady state solutions for nonlinear circuits particularly circuits containing distributed elements such as transmission lines Examining the problem of computing steady state solutions in this context has led to a collection of novel numerical algorithms which we have gathered along with some background material into this book Although we wished to appeal to as broad an audience as possible to treat the subject in depth required maintaining a narrow focus Our compromise was to assume that the reader is familiar with basic numerical methods such as might be found in dahlquist74 or vlach83 but not assume any specialized knowledge of methods for steady state problems Although we focus on algorithms for computing steady state solutions of analog and microwave circuits the methods herein are general in nature and may find use in other

disciplines A number of new algorithms are presented the contributions primarily centering around new approaches to harmonic balance and mixed frequency time methods These methods are described along with appropriate background material in what we hope is a reasonably satisfying blend of theory practice and results The theory is given so that the algorithms can be fully understood and their correctness established **Gallium Arsenide Digital Circuits** Omar Wing,2012-12-06 Gallium Arsenide technology has come of age GaAs integrated circuits are available today as gate arrays with an operating speed in excess of one Gigabits per second Special purpose GaAs circuits are used in optical fiber digital communications systems for the purpose of regeneration multiplexing and switching of the optical signals As advances in fabrication and packaging techniques are made the operat ing speed will further increase and the cost of production will reach a point where large scale application of GaAs circuits will be economical in these and other systems where speed is paramount This book is written for students and engineers who wish to enter into this new field of electronics for the first time and who wish to embark on a serious study of the subject of GaAs circuit design No prior knowledge of GaAs technology is assumed though some previous experience with MOS circuit design will be helpful A good part of the book is devoted to circuit analysis to the extent that is possible for non linear circuits The circuit model of the GaAs transistor is derived from first principles and analytic formulas useful in predicting the approxi mate circuit performance are also derived Computer simulation is used throughout the book to show the expected performance and to study the effects of parameter variations

Mixed-Mode Simulation Resve A. Saleh,A. Richard Newton,2012-12-06 Our purpose in writing this book was two fold First we wanted to compile a chronology of the research in the field of mixed mode simulation over the last ten to fifteen years A substantial amount of work was done during this period of time but most of it was published in archival form in Masters theses and Ph D dissertations Since the interest in mixed mode simulation is growing and a thorough review of the state of the art in the area was not readily available we thought it appropriate to publish the information in the form of a book Secondly we wanted to provide enough information to the reader so that a proto type mixed mode simulator could be developed using the algorithms in this book The SPLICE family of programs is based on the algorithms and techniques described in this book and so it can also serve as docu mentation for these programs ACKNOWLEDGEMENTS The authors would like to dedicate this book to Prof D O Peder son for inspiring this research work and for providing many years of support and encouragement The authors enjoyed many fruitful discus sions and collaborations with Jim Kleckner Young Kim Alberto Sangiovanni Vincentelli and Jacob White and we thank them for their contributions We also thank the countless others who participated in the research work and read early versions of this book Lillian Beck provided many useful suggestions to improve the manuscript Yun cheng Ju did the artwork for the illustrations **PARLE '92, Parallel Architectures and Languages Europe** Daniel Etiemble,1992 The 1992 Parallel Architectures and Languages Europe conference continues the tradition of a wide and representative international meeting of specialists from academia and

industry in theory design and application of parallel computer systems set by the previous PARLE conferences held in Eindhoven in 1987 1989 and 1991 This volume contains the 52 regular and 25 poster papers that were selected from 187 submitted papers for presentation and publication In addition five invited lectures areincluded The regular papers are organized into sections on implementation of parallel programs graph theory architecture optimal algorithms graph theory and performance parallel software components data base optimization and modeling data parallelism formal methods systolic approach functional programming fine grain parallelism Prolog data flow systems network efficiency parallel algorithms cache systems implementation of parallel languages parallel scheduling in data base systems semantic models parallel data base machines and language semantics PUBLISHER S WEBSITE Parallel Architectures for Data/knowledge-based Systems L. L. Miller,A. R. Hurson,1995 **Parallel Architectures for Image Processing** Joydeep Ghosh,Colin G. Harrison,1990 *The Third Conference on Hypercube Concurrent Computers and Applications: Architecture, software, computer systems, and general issues* Geoffrey C. Fox,1988 **Proceedings of the ... International Conference on Microelectronics** ,2001 **1993 Computer Architectures for Machine Perception** Magdy A. Bayoumi,Larry S. Davis,Kimon Valavanis,1993 Proceedings of the Computer Architectures for Machine Perception Workshop held Dec 15 17 1993 in New Orleans Louisiana Papers came from several communities computer architecture pattern recognition image processing and analysis computer vision and VLSI No index Annotation copyright Book N **Introduction to Parallel Computing** Vipin Kumar,1994 Mathematics of Computing Parallelism Computer Sciences Technical Report ,1994

Proceedings of the 1987 International Conference on Parallel Processing Sartaj Sahni,1987 Interest in the field of parallel processing has soared in this decade Response to the annual conference held at Pheasant Run is clear proof in the early 1980s just over 100 papers were submitted each year in 1987 over 400 papers were considered Such heightened interest is both gratifying and challenging in that the increasingly demanding applications in science medicine and industry that result from these studies are dependent upon the parallel systems as computing resources The studies in this volume describe recent advances in all aspects of parallel distributed logic circuits impact of VLSI to parallel processor architecture various concurrent distributed parallel pipeline or multiple processor architectures processor memory interconnections computer networks distributed databases reliability and fault tolerance modeling and simulation techniques performance measurements operating systems languages algorithms mathematical analysis and various application studies A rigorous standard governed the paper selection process in an effort to insure that the proceedings reflect the state of the art in parallel processing theory design and applications Of vital interest to researchers engineers scientists programmers systems analysts managers and other interested in the design and application of parallel distributed processors and processing

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