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Vlsi Test Symposium Proceedings

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Vlsi Test Symposium Proceedings:

VLSI Test Symposium: Proceedings: IEEE VLSI Test Symposium (17th: 1999: Dana Point, CA. IEEE Staff, 1999 Proceedings Ieee, 2004 The proceedings of the 21st IEEE VLSI test symposium VTS 2003 describing innovations in the testing of integrated circuits and systems **26th IEEE VLSI Test Symposium** ,2008 VLSI Test Symposium, 1995. Proceedings., 13th IEEE. , **17th IEEE VLSI Test Symposium** ,1999 Proceedings ,1994 Proceedings of the symposium held in Cherry Hill New Jersey April 1994 Technical sessions are devoted to synthesis and testability testable mixed signal circuit designs built in self test test generation on line testing defect coverage advanced test generat *19th IEEE VLSI Test Symposium* ,2001 **2009 27th IEEE VLSI Test Symposium** ,2009 2011 29th IEEE VLSI Test Symposium ,2011 **2010 28th IEEE VLSI Test Symposium** ,2010 22nd IEEE VLSI Test Symposium ,2004 *Les Coustumes générales de toute la conté d'Artois* ,1726 *Proceedings* ,2002 20th IEEE VLSI Test Symposium (VTS 2002) ,2002 **19th IEEE VLSI Test Symposium, VTS 2001** ,2001 Proceedings of 17th IEEE VLSI Test Symposium ,1999 **21st IEEE VLSI Test Symposium** ,2003-01-01 VTS 2003 is the twenty first in a series of annual symposia that explore the state of the art and introduce innovative approaches in the testing of electronic circuits and systems The complexity of current generation ICs combined with rapidly developing high density high speed packaging and reduced design cycle time has made it extremely difficult and expensive to comprehensively test electronic systems and diagnose failed parts using traditional methods This situation worsens as we move toward nanometer technologies **24th IEEE VLSI Test Symposium** ,2006 VTS 2006 focuses on innovation in the field of testing of integrated circuits and systems VTS looks at the many trends and challenges in the semiconductor design and manufacturing industries with papers covering wide range of interests including basic and continuing education for test professionals the latest research developments new directions and hot topics in test and expert perspectives on current issues The proceedings explores high speed interconnect testing analog and mixed signal testing delay testing flash and memory testing RF testing yield analysis nanoscale testing IDDQ MEMS and wireless testing **IEEE 29th VLSI Test Symposium (VTS), 2011** ,2011 *25th IEEE VLSI Test Symposium* ,2007 Annotation VTS 2007 focuses on innovation in the field of testing of integrated circuits and systems The core of VTS 2007 explores the many trends and challenges in the semiconductor design and manufacturing industries with papers covering a diverse and seminal set of topics including RF and Analog Test Delay Test Memory Test Diagnosis Online Test SOC Test and Fault Prediction and Evaluation The proceedings features special sessions and covers innovative practices highlighting cutting edge challenges faced by test practitioners and innovative solutions Contents RF Test Delay Test Quality Memory Test Test Compression Going after Defects Online Test Diagnosis ATPG for Delay Faults Advances in Test Failure Estimation Fault Prediction Analog Test High Level Test Techniques Memory Repair SOC Test Design for Test Testing Large Chips Ensuring Secure Chips

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