System-Level Power-Aware Design Techniques in Real-Time Systems

Osman S. Unsal, Israel Koren, System-Level Power-Aware Design Techniques in Real-Time Systems, Proceedings of IEEE, Vol. 91, No. 7, July 2003.

Systemlevel Design Techniques For Energyefficient Embedded Systems

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algorithms and applications computer networks cyber trust and security wireless networks as well as mobile computing and bioinformatics *Energy-Efficient Embedded System Design* Wenjie Huang,2021 The pursuit of energy efficient design in embedded systems has long become a critical issue With improved energy effciency the systems can incorporate more functionality and support better performances Conventional design techniques innovate in hierarchical design levels from system algorithm architecture to circuit However with the slowing of Moore s effect efforts other than the circuit level design are becoming more promising for the emerging applications In this work we investigate the core signal processing units in wireless communication systems and introduce a suite of new techniques from algorithm to architecture levels to improve energy effciency First we develop a comprehensive message truncation scheme to mitigate the decoding complexity of non binary LDPC decoders The dynamic channel state is exploited in the initialization stage to reduce message length We then further prune the messages employing the inter iteration decoding state of the core computational unit The arithmetical logic and memory usage could be substantially decreased and therefore reduces the decoder power with the shorter messages We also propose an adaptive offset correction mechanism to minimize the possible performance loss due to message truncation And we develop a novel decoder architecture to accommodate the proposed algorithm designs Second we introduce a new non binary LDPC decoder architecture with a low power memory unit As non binary LDPC decoding is memory intensive and more than half of the power is consumed by memory access the decoder power decreases significantly with the reduced memory power Although over scaling in memory power may introduce soft errors LDPC codes could correct them with the error resilience as channel codes To find the extent to scale memory power we train the decoder with the performance constraint under given channel states before the exploitations Finally we investigate the optimal sequential control policy for the signal tracking of GNSS receivers powered by renewable energy With the proposed greedy and reinforcement learning algorithm the receiver could opportunistically utilize the harvested energy by jointly considering the signal noise ratio of the received signal and the available energy level Different than conventional efforts we could significantly maximize both energy efficiency and system service time with the desired positioning performances **Ambient Intelligence** Werner Weber,Jan Rabaey,Emile H.L. Aarts,2005-12-12 Ambient intelligence is the vision of a technology that will become invisibly embedded in our natural surroundings present whenever we need it enabled by simple and effortless interactions attuned to all our senses adaptive to users and context sensitive and autonomous High quality information access and personalized content must be available to everybody anywhere and at any time This book addresses ambient intelligence used to support human contacts and accompany an individual s path through the complicated modern world From the technical standpoint distributed electronic intelligence is addressed as hardware vanishing into the background Devices used for ambient intelligence are small low power low weight and very importantly low cost they collaborate or interact with each other and they are redundant and error tolerant This means that the failure of one device will not cause failure of the whole system Since wired

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