Why Programs Fail A Guide To Systematic Debugging

Lingjun Ying

Why Programs Fail A Guide To Systematic Debugging:

Why Programs Fail Andreas Zeller,2006 Debugging has undergone a sea change in recent years Increasing processing power has allowed for the creation of much more sophisticated software tools for the analysis and debugging of programs This in turn has allowed debugging to graduate from a black art to a systematic discipline The time is right to summarize this new advanced approach to debugging Why Programs Fail is about bugs in computer programs how to reproduce them how to find them and how to fix them in such a way that they do not occur anymore This is the first comprehensive book on systematic debugging and covers a wide range of tools and techniques ranging from hands on observation to fully automated diagnoses and includes instructions for building automated debuggers This discussion is built upon a solid theory of how failures occur rather than relying on seat of the pants techniques which are of little help with large software systems or to those learning to program Andreas Zeller is well known in the programming community for developing the GNU Data Display Debugger DDD a tool that visualizes the data structures of a program while it is running Over 250 000 users as well as all major software companies use DDD for software development Zeller is also the creator of the popular Ask Igor website that will automatically debug programs over the web and he regularly speaks and consults in the USA Why Programs Fail, 2nd Edition Andreas Zeller,2009 Why Programs Fail A Guide to Systematic Debugging is proof that debugging has graduated from a black art to a systematic discipline It demystifies one of the toughest aspects of software programming showing clearly how to discover what caused software failures and fix them with minimal muss and fuss The fully updated second edition includes 100 pages of new material including new chapters on Verifying Code Predicting Erors and Preventing Errors Cutting edge tools such as FindBUGS and AGITAR are explained techniques from integrated environments like Jazz net are highlighted and all new demos with ESC Java and Spec Eclipse and Mozilla are included This complete and pragmatic overview of debugging is authored by Andreas Zeller the talented researcher who developed the GNU Data Display Debugger DDD a tool that over 250 000 professionals use to visualize the data structures of programs while they are running Unlike other books on debugging Zeller s text is product agnostic appropriate for all programming languages and skill levels The book explains best practices ranging from systematically tracking error reports to observing symptoms reproducing errors and correcting defects It covers a wide range of tools and techniques from hands on observation to fully automated diagnoses and also explores the author s innovative techniques for isolating minimal input to reproduce an error and for tracking cause and effect through a program It even includes instructions on how to create automated debugging tools The text includes exercises and extensive references for further study and a companion website with source code for all examples and additional debugging resources is available The new edition of this award winning productivity booster is for any developer who has ever been frustrated by elusive bugs Brand new chapters demonstrate cutting edge debugging techniques and tools enabling readers to put the latest time saving developments to work for them Learn by doing New exercises and detailed

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Debugging at the Electronic System Level Frank Rogin,Rolf Drechsler,2010-06-17 Debugging becomes more and more the bottleneck to chip design productivity especially while developing modern complex integrated circuits and systems at the Electronic System Level ESL Today debugging is still an unsystematic and lengthy process Here a simple reporting of a failure is not enough anymore Rather it becomes more and more important not only to find many errors early during development but also to provide efficient methods for their isolation In Debugging at the Electronic System Level the state of the art of modeling and verification of ESL designs is reviewed There a particular focus is taken onto SystemC Then a reasoning hierarchy is introduced The hierarchy combines well known debugging techniques with whole new techniques to improve the verification efficiency at ESL The proposed systematic debugging approach is supported amongst others by static code analysis debug patterns dynamic program slicing design visualization property generation and automatic failure isolation All techniques were empirically evaluated using real world industrial designs Summarized the introduced approach enables a systematic search for errors in ESL designs Here the debugging techniques improve and accelerate error detection

observation and isolation as well as design understanding Debugging Systems-on-Chip Bart Vermeulen,Kees Goossens,2014-07-14 This book describes an approach and supporting infrastructure to facilitate debugging the silicon implementation of a System on Chip SOC allowing its associated product to be introduced into the market more quickly Readers learn step by step the key requirements for debugging a modern silicon SOC implementation nine factors that complicate this debugging task and a new debug approach that addresses these requirements and complicating factors The authors novel communication centric scan based abstraction based run stop based CSAR debug approach is discussed in detail showing how it helps to meet debug requirements and address the nine previously identified factors that complicate debugging silicon implementations of SOCs The authors also derive the debug infrastructure requirements to support debugging of a silicon implementation of an SOC with their CSAR debug approach This debug infrastructure consists of a generic on chip debug architecture a configurable automated design for debug flow to be used during the design of an SOC and customizable off chip debugger software Coverage includes an evaluation of the efficiency and effectiveness of the CSAR approach and its supporting infrastructure using six industrial SOCs and an illustrative example SOC model The authors also quantify the hardware cost and design effort to support their approach **Embedded Software Verification and Debugging** Djones Lettnin,Markus Winterholer,2017-04-17 This book provides comprehensive coverage of verification and debugging techniques for embedded software which is frequently used in safety critical applications e g automotive where failures are unacceptable Since the verification of complex systems needs to encompass the verification of both hardware and embedded software modules this book focuses on verification and debugging approaches for embedded software with hardware dependencies Coverage includes the entire flow of design verification and debugging of embedded software and all key approaches to debugging dynamic static and hybrid verification This book discusses the current industrial embedded software verification flow as well as emerging trends with focus on formal and hybrid verification and debugging approaches

Handbook of Software Fault Localization W. Eric Wong,T.H. Tse,2023-05-02 Handbook of Software Fault Localization A comprehensive analysis of fault localization techniques and strategies In Handbook of Software Fault Localization Foundations and Advances distinguished computer scientists Prof W Eric Wong and Prof T H Tse deliver a robust treatment of up to date techniques tools and essential issues in software fault localization The authors offer collective discussions of fault localization strategies with an emphasis on the most important features of each approach The book also explores critical aspects of software fault localization like multiple bugs successful and failed test cases coincidental correctness faults introduced by missing code the combination of several fault localization techniques ties within fault localization rankings concurrency bugs spreadsheet fault localization and theoretical studies on fault localization Readers will benefit from the authors straightforward discussions of how to apply cost effective techniques to a variety of specific environments common in the real world They will also enjoy the in depth explorations of recent research directions on this topic Handbook of Software

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