



За день За период

Октябрь 2010

Пн	Вт	Ср	Чт	Пт	Сб	Вс
27	28	29	30	31	2	3
4	5	6	7	8	9	10
11	12	13	14	15	16	17
18	19	20	21	22	23	24
25	26	27	28	29	30	31
1	2	3	4	5	6	7

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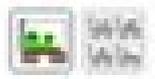
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 - Андрей
 - Николай
 - Компьютер Николай
 - Николай
 - Компьютер Сергей
 - Андрей
- Отдел маркетинга
 - Компьютер Вова
 - Компьютер Володя
 - Владимир

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Активность работы сотрудника

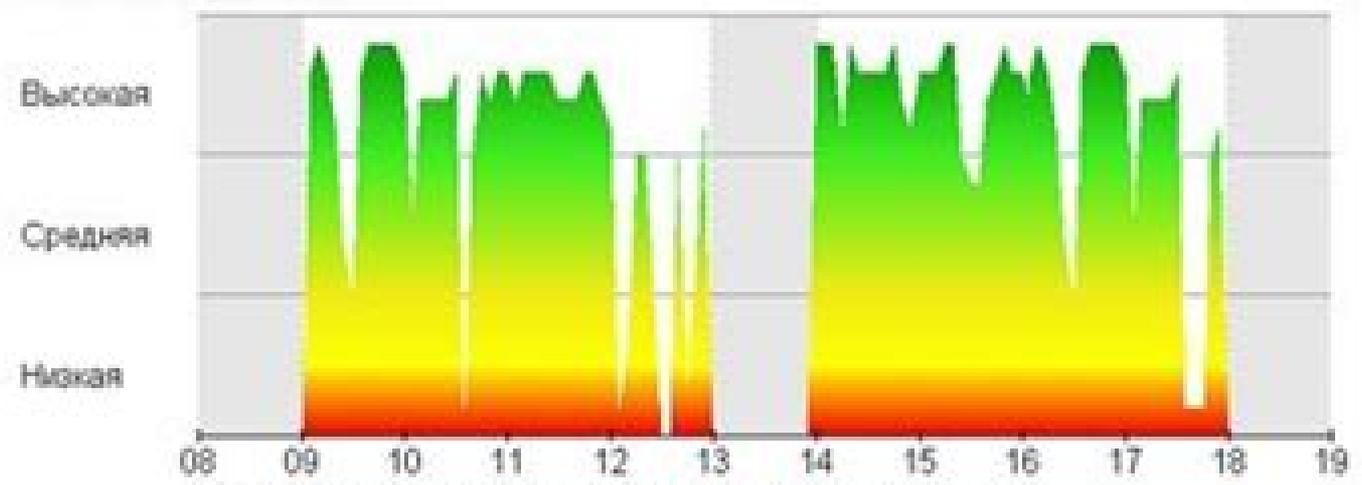
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График активности работы



Workview Office

Yicheng Fang



Workview Office:

Workview Office Student Edition R. James Duckworth, 1997 This edition combines realistic logic design exercises and computer aided engineering software from Viewlogic Systems The class tested laboratory exercises provide realistic examples of combinational and sequential circuits and are used in a tutorial manner to demonstrate how to use the design entry and analysis tools of Workview Office Students are given access to the same type and versions of design tools used by practicing engineers allowing them to spend their time developing a sound understanding of theory and design principles rather than wasting time manually constructing and debugging circuits in the lab They can draw circuit designs using the ViewDraw schematic editor and then perform extensive analysis using the ViewSim gate level simulator As the circuit is tested ViewSim back annotates the circuit drawing with the new digital logic values at all nodes providing solid feedback on how a circuit is actually performing during tests and making fault finding easy

Neural and Fuzzy Logic Control of Drives and Power Systems Marcian Cirstea, 2002-10-08 Introduces cutting edge control systems to a wide readership of engineers and students The first book on neuro fuzzy control systems to take a practical applications based approach backed up with worked examples and case studies Learn to use VHDL in real world applications Introducing cutting edge control systems through real world applications Neural networks and fuzzy logic based systems offer a modern control solution to AC machines used in variable speed drives enabling industry to save costs and increase efficiency by replacing expensive and high maintenance DC motor systems The use of fast micros has revolutionised the field with sensorless vector control and direct torque control This book reflects recent research findings and acts as a useful guide to the new generation of control systems for a wide readership of advanced undergraduate and graduate students as well as practising engineers The authors guide readers quickly and concisely through the complex topics of neural networks fuzzy logic mathematical modelling of electrical machines power systems control and VHDL design Unlike the academic monographs that have previously been published on each of these subjects this book combines them and is based round case studies of systems analysis control strategies design simulation and implementation The result is a guide to applied control systems design that will appeal equally to students and professional design engineers The book can also be used as a unique VHDL design aid based on real world power engineering applications

Proceedings, 1997 **Engineering Digital Design** Richard F. Tinder, 2000-01-18 Engineering Digital Design Second Edition provides the most extensive coverage of any available textbook in digital logic and design The new REVISED Second Edition published in September of 2002 provides 5 productivity tools free on the accompanying CD ROM This software is also included on the Instructor s Manual CD ROM and complete instructions accompany each software program In the REVISED Second Edition modern notation combines with state of the art treatment of the most important subjects in digital design to provide the student with the background needed to enter industry or graduate study at a competitive level Combinatorial logic design and synchronous and asynchronous sequential

machine design methods are given equal weight and new ideas and design approaches are explored The productivity tools provided on the accompanying CD are outlined below

- 1 EXL Sim2002 logic simulator EXL Sim2002 is a full featured interactive schematic capture and simulation program that is ideally suited for use with the text at either the entry or advanced level of logic design Its many features include drag and drop capability rubber banding mixed logic and positive logic simulations macro generation individual and global or randomized delay assignments connection features that eliminate the need for wire connections schematic page sizing and zooming waveform zooming and scrolling a variety of printout capabilities and a host of other useful features
- 2 BOOZER logic minimizer BOOZER is a software minimization tool that is recommended for use with the text It accepts entered variable EV or canonical 1 s and 0 s data from K maps or truth tables with or without don t cares and returns an optimal or near optimal single or multi output solution It can handle up to 12 functions Boolean functions and as many inputs when used on modern computers
- 3 ESPRESSO II logic minimizer ESPRESSO II is another software minimization tool widely used in schools and industry It supports advanced heuristic algorithms for minimization of two level multi output Boolean functions but does not accept entered variables It is also readily available from the University of California Berkeley 1986 VLSI Tools Distribution
- 4 ADAM design software ADAM for Automated Design of Asynchronous Machines is a very powerful productivity tool that permits the automated design of very complex asynchronous state machines all free of timing defects The input files are state tables for the desired state machines The output files are given in the Berkeley format appropriate for directly programming PLAs ADAM also allows the designer to design synchronous state machines timing defect free The options include the lumped path delay LPD model or NESTED CELL model for asynchronous FSM designs and the use of D FLIP FLOPs for synchronous FSM designs The background for the use of ADAM is covered in Chapters 11 14 and 16 of the REVISED 2nd Edition
- 5 A OPS design software A OPS for Asynchronous One hot Programmable Sequencers is another very powerful productivity tool that permits the design of asynchronous and synchronous state machines by using a programmable sequencer kernel This software generates a PLA or PAL output file in Berkeley format or the VHDL code for the automated timing defect free designs of the following a Any 1 Hot programmable sequencer up to 10 states b The 1 Hot design of multiple asynchronous or synchronous state machines driven by either PLDs or RAM The input file is that of a state table for the desired state machine This software can be used to design systems with the capability of instantly switching between several radically different controllers on a time shared basis The background for the use of A OPS is covered in Chapters 13 14 and 16 of the REVISED 2nd Edition

Implementing Digital Designs Using PLDS Tracie Yvette Green,1997 *Electronic Engineering* ,1996

Electronic Design ,1997 **Programmable Logic Data Book 1997** ,1997 *EDN* ,1996 Network-Based Parallel Computing Communication, Architecture, and Applications Anand Sivasubramaniam,Mario Lauria,1999-05-05 This book constitutes the thoroughly refereed post workshop proceedings of the Third International Workshop on Communication

Architecture and Applications for Network Based Parallel Computing CANPC 99 held in Orlando Florida in January 1999 The 15 revised full papers presented have been carefully reviewed and selected for inclusion in the book They present the state of the art in network based parallel computing employing clusters of workstations PCs connected by off the shelf networks for cost effective high performance computing VHDL Starter's Guide Sudhakar Yalamanchili,1998 VHDL Starter s Guide has been written for the student and practitioner alike as a clear and concise tutorial on VHDL VHSIC Hardware Description Language It provides a hands on step by step introduction to learning VHDL as an applied language to be used in the design and testing of digital logic networks Command syntax and structure are emphasized and the writing is based on many examples of real world logic circuits **Electronic Business** ,1990 *The Proceedings of the Thirtieth SIGCSE Technical Symposium on Computer Science Education* Jane Prey,SIGCSE99: The 30th ACM SIGCSE Technical Symposium,1999 **The Proceedings of the Thirtieth SIGCSE Technical Symposium on Computer Science Education** Daniel T. Joyce,1999 Electronic Business Today ,1995 *Ross's Business English* John Walter Ross,1915 Aerospace America ,1999 EDN, Electrical Design News ,1998 *Modern Cemetery* ,1901 *Who is who on the Bulgarian Computer Market* ,2006

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Table of Contents Workview Office

1. Understanding the eBook Workview Office
 - The Rise of Digital Reading Workview Office
 - Advantages of eBooks Over Traditional Books
2. Identifying Workview Office
 - Exploring Different Genres
 - Considering Fiction vs. Non-Fiction
 - Determining Your Reading Goals
3. Choosing the Right eBook Platform
 - Popular eBook Platforms
 - Features to Look for in an Workview Office
 - User-Friendly Interface
4. Exploring eBook Recommendations from Workview Office
 - Personalized Recommendations
 - Workview Office User Reviews and Ratings
 - Workview Office and Bestseller Lists
5. Accessing Workview Office Free and Paid eBooks
 - Workview Office Public Domain eBooks
 - Workview Office eBook Subscription Services
 - Workview Office Budget-Friendly Options

6. Navigating Workview Office eBook Formats
 - ePub, PDF, MOBI, and More
 - Workview Office Compatibility with Devices
 - Workview Office Enhanced eBook Features
7. Enhancing Your Reading Experience
 - Adjustable Fonts and Text Sizes of Workview Office
 - Highlighting and Note-Taking Workview Office
 - Interactive Elements Workview Office
8. Staying Engaged with Workview Office
 - Joining Online Reading Communities
 - Participating in Virtual Book Clubs
 - Following Authors and Publishers Workview Office
9. Balancing eBooks and Physical Books Workview Office
 - Benefits of a Digital Library
 - Creating a Diverse Reading Collection Workview Office
10. Overcoming Reading Challenges
 - Dealing with Digital Eye Strain
 - Minimizing Distractions
 - Managing Screen Time
11. Cultivating a Reading Routine Workview Office
 - Setting Reading Goals Workview Office
 - Carving Out Dedicated Reading Time
12. Sourcing Reliable Information of Workview Office
 - Fact-Checking eBook Content of Workview Office
 - Distinguishing Credible Sources
13. Promoting Lifelong Learning
 - Utilizing eBooks for Skill Development
 - Exploring Educational eBooks
14. Embracing eBook Trends
 - Integration of Multimedia Elements

- Interactive and Gamified eBooks

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