



System Level Design Of Reconfigurable Systems On Chips

Tony Digaleh Givargis



System Level Design Of Reconfigurable Systems On Chips:

System Level Design of Reconfigurable Systems-on-Chip Nikolaos Voros, Konstantinos Masselos, 2006-02-23 System Level Design of Reconfigurable Systems on Chip provides insight in the challenges and difficulties encountered during the design of reconfigurable Systems on Chip SoCs Reconfiguration is becoming an important part of System on Chip design to cope with the increasing demands for simultaneous flexibility and computational power The book focuses on system level design issues for reconfigurable SoCs and provides information on reconfiguration aspects of complex SoCs and how they can be implemented in practice It is divided in three parts The first part provides background information and requirements on reconfigurable technologies and systems The second one identifies existing methodological gaps and introduces a design flow for developing reconfigurable Systems on Chip The high level part of the design flow can be covered by two C based methodologies one based on SystemC and one based on OCAPI XL both including appropriate extensions to handle reconfiguration issues Finally the third part of the book presents reconfigurable SoCs from the perspective of the designer through three indicative case studies from the wireless and multimedia communication domain

Reconfigurable System Design and Verification Pao-Ann Hsiung, Marco D. Santambrogio, Chun-Hsian Huang, 2018-10-08 Reconfigurable systems have pervaded nearly all fields of computation and will continue to do so for the foreseeable future Reconfigurable System Design and Verification provides a compendium of design and verification techniques for reconfigurable systems allowing you to quickly search for a technique and determine if it is appropriate to the task at hand It bridges the gap between the need for reconfigurable computing education and the burgeoning development of numerous different techniques in the design and verification of reconfigurable systems in various application domains The text explains topics in such a way that they can be immediately grasped and put into practice It starts with an overview of reconfigurable computing architectures and platforms and demonstrates how to develop reconfigurable systems This sets up the discussion of the hardware software and system techniques that form the core of the text The authors classify design and verification techniques into primary and secondary categories allowing the appropriate ones to be easily located and compared The techniques discussed range from system modeling and system level design to co simulation and formal verification Case studies illustrating real world applications detailed explanations of complex algorithms and self explaining illustrations add depth to the presentation Comprehensively covering all techniques related to the hardware software design and verification of reconfigurable systems this book provides a single source for information that otherwise would have been dispersed among the literature making it very difficult to search compare and select the technique most suitable The authors do it all for you making it easy to find the techniques that fit your system requirements without having to surf the net or digital libraries to find the candidate techniques and compare them yourself

The Design and Implementation of a 200k-gate Reconfigurable Computing System Feng Yun, 1997 *Proceedings of ASP-DAC/VLSI Design 2002*, 2002 Papers from a January 2002 conference are

organized into four sessions each on low power design synthesis testing layout and interconnects and technology as well as two sessions each on embedded systems verification and VLSI architecture one session on analog design and one session on hot c

Reconfigurable Systems Institution of Electrical Engineers,1999 *Proceedings of the 2002 International Workshop on System-level Interconnect Prediction* ,2002 **Proceedings of the ASP-DAC ... Asia and South Pacific Design Automation Conference** ,2002 **Introduction to Reconfigurable Computing** Christophe Bobda,2007-09-30

Introduction in Reconfigurable Computing provides a comprehensive study of the field Reconfigurable Computing It provides an entry point to the novice willing to move in the research field reconfigurable computing FPGA and system on programmable chip design The book can also be used as teaching reference for a graduate course in computer engineering or as reference to advance electrical and computer engineers It provides a very strong theoretical and practical background to the field of reconfigurable computing from the early Estrin s machine to the very modern architecture like coarse grained reconfigurable device and the embedded logic devices Apart from the introduction and the conclusion the main chapter of the book are the following Architecture of reconfigurable systems which presents the technology and the architecture used in fined grained and those used in coarse grained reconfigurable devices Design and implementation This section deals with the implementation on reconfigurable system It briefly covers the steps needed to implement application on today s FPGAs And focus on the logic synthesis for FPGA in particular LUT technology mapping High Level Synthesis for Reconfigurable Devices The high level synthesis for reconfigurable systems also known as temporal partitioning is presented here Several temporal partitioning techniques are presented and explained Temporal placement This section considers stand alone reconfigurable systems Its assume that a kind of operating systems for reconfigurable systems is in charge of managing the resources of a given system and allocate space on a device for the computation of incoming tasks and therefore presents several temporal placement approaches for off line as well as on line placement On line and Dynamic Interconnection This chapter reviews andexplains the different approaches for allowing communication between modules dynamically placed at run time on a given device Designing a reconfigurable application on Xilinx Virtex FPGA In this section the different design approaches of partial reconfigurable systems on the Xilinx FPGAs that are one of the few one on the market with this feature is explained System on programmable chip System on programmable chip is a hot topic in reconfigurable computing This is mainly the integration of a system made upon some peripheral UART Ethernet VGA etc but also computational Coding filter etc hardware modules on one programmable chip The current usable solutions are presented The book furthermore focusses on the development of adaptive multiprocessors on chip i e systems consisting of a set of Processors and exchangeable hardware accelerators Applications This part covers the use of reconfigurable system in computer architecture rapid prototyping reconfigurable supercomputer reconfigurable massively parallel computers and algorithm better adapted for reconfigurable systems distributed arithmetic network packet processing etc Journal of VLSI Signal Processing Systems

for *Signal, Image, and Video Technology*, 1999 *IEEE Circuits & Devices*, 2006 **System-level Exploration for Pareto-optimal Configurations in Parameterized System-on-a-chip Architectures** Tony Digaleh Givargis, 2001
Parallel and Distributed Processing, 1998 **IEEE/ACM/IFIP International Conference on Hardware/Software Codesign & System Synthesis**, 2003 **14th Symposium on Integrated Circuits and Systems Design** Sociedade Brasileira de Computação, 2001 Annotation Papers from a September 2001 symposium report on recent advances in areas of integrated circuits and systems design including embedded systems rapid prototyping formal methods codesign CAD and test analog digital and physical design and low power and low voltage Specific topics include communication architectures for system on chip using the CAN protocol and reconfigurable computing technology for Web based smart house automation and optimizing BDD based verification analyzing variable dependencies Other subjects include interconnection length estimation at logic level an environment to aid the synthesis of threephase analogue waveform using AHDL and extending sequencing graphs for reconfigurable applications modeling This work lacks a subject index c Book News Inc *IEEE International High-Level Design Validation and Test Workshop*, 2003 **VLSI: Systems on a Chip** Luis Miguel Silveira, Srinivas Devadas, Ricardo A. Reis, 1999-12-31 The current trend towards the realization of complex and versatile Systems on a Chip requires the combined efforts and attention of experts in a wide range of areas including microsystems embedded hardware software systems dedicated ASIC and programmable logic hardware reconfigurable computing wireless communications and RF issues video and image processing memory systems low power design techniques design test and verification algorithms modeling and simulation logic synthesis and interconnect analysis Thus the contributions presented herein address a wide range of Systems on a Chip problems VLSI Systems on a Chip comprises the selected proceedings of the Tenth International Conference on Very Large Scale Integration VLSI 99 which was sponsored by the International Federation for Information Processing IFIP and was held in Lisbon Portugal in December 1999 The volume is organized around two themes in which the following topics are addressed VLSI Systems Design and Applications Analog Systems Design Analog Modeling and Design Image Processing Reconfigurable Computing Memory and System Design Low Power Design VLSI Design Methods and CAD Test and Verification Analog CAD and Interconnect Fundamental CAD Algorithms Verification and Simulation CAD for Physical Design High Level Synthesis and Verification of Embedded Systems VLSI Systems on a Chip is essential reading for researchers working on system integration design and CAD First IEEE/ACM/IFIP International Conference on Hardware/Software Codesign & System Synthesis, 2003 **CODES+ISSS**, 2004 *XI Brazilian Symposium on Integrated Circuit Design* Marcelo Lubaszewski, Vladimir Castro Alves, 1998 Topics in this book on integrated circuit design include hardware software codesign of embedded systems the ALFA HUERTA project rapid prototyping digital testing and digital design **Design of Microprocessor-based Systems** Nikitas A. Alexandridis, 1993

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