

SystemC Kernel Extensions for Heterogeneous System Modeling

A Framework for Multi-MoC Modeling & Simulation

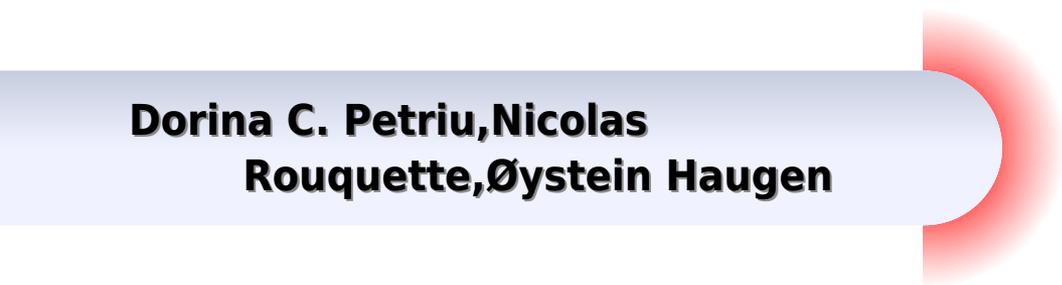
By

Hiren D. Patel and Sandeep K. Shukla

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Systemc Kernel Extensions For Heterogenous System Modeling

**Dorina C. Petriu, Nicolas
Rouquette, Øystein Haugen**



SystemC Kernel Extensions For Heterogeneous System Modeling:

SystemC Kernel Extensions for Heterogeneous System Modeling Hiren Patel, Sandeep Kumar Shukla, 2006-01-16

SystemC Kernel Extensions for Heterogeneous System Modeling is a result of an almost two year endeavour on our part to understand how SystemC can be made useful for system level modeling at higher levels of abstraction Making it a truly heterogeneous modeling language and platform for hardware software co design as well as complex embedded hardware designs has been our focus in the work reported in this book

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Synthesis of Embedded Software Sandeep Kumar Shukla, Jean-Pierre Talpin, 2010-08-05 Embedded software is ubiquitous today There are millions of lines of embedded code in smart phones and even more in systems responsible for automotive control avionics control weapons control and space missions Some of these are safety critical systems whose correctness timely response and reliability are of paramount importance These requirements pose new challenges to system designers This necessitates that a proper design science based on constructive correctness be developed Correct by construction design and synthesis of embedded software is done in a way so that post development verification is minimized and correct operation of embedded systems is maximized This book presents the state of the art in the design of safety critical embedded software It introduces readers to three major approaches to specification driven embedded software synthesis construction synchronous programming based approaches models of computation based approaches and an approach based on concurrent programming with a co design focused language It is an invaluable reference for practitioners and researchers concerned with improving the product development life cycle

Embedded Systems Design Based on Formal Models of Computation Ivan Radojevic, Zoran Salcic, 2011-06-15 Models of Computation for Heterogeneous Embedded Systems presents a model of computation for heterogeneous embedded systems called DFCharts It targets heterogeneous systems by combining finite state machines FSM with synchronous dataflow graphs SDFG FSMs are connected in the same way as in Argos a Statecharts variant with purely synchronous semantics using three operators synchronous parallel refinement and hiding The fourth operator called asynchronous parallel is introduced in DFCharts to connect FSMs with SDFGs In the formal semantics of DFCharts the operation of an SDFG is represented as an FSM Using this representation SDFGs are merged with FSMs so that the behaviour of a complete DFCharts specification can be expressed as a single flat FSM This allows system properties to be verified globally The practical application of DFCharts has been demonstrated by linking it to widely used system level languages Java Esterel and SystemC

Embedded Systems Specification and Design Languages Eugenio

Villar,2008-05-15 This book is the latest contribution to the Chip Design Languages series and it consists of selected papers presented at the Forum on Specifications and Design Languages FDL 07 in September 2007 The book represents the state of the art in research and practice and it identifies new research directions It highlights the role of specification and modelling languages and presents practical experiences with specification and modelling languages **Metamodeling-driven IP**

Reuse for SoC Integration and Microprocessor Design Deepak A. Mathaikutty,Sandeep Shukla,2009 This cutting edge resource offers you an in depth understanding of metamodeling approaches for the reuse of intellectual properties IPs in the form of reusable design or verification components The book covers the essential issues associated with fast and effective integration of reusable design components into a system on a chip SoC to achieve faster design turn around time Moreover it addresses key factors related to the use of reusable verification IPs for a write once use many times verification strategy another effective approach that can attain a faster product design cycle **Model Driven Engineering Languages and Systems**

Dorina C. Petriu,Nicolas Rouquette,Øystein Haugen,2010-09-27 This book is part II of a two volume work that contains the refereed proceedings of the 13th International Conference on Model Driven Engineering Languages and Systems MODELS 2010 held in Oslo Norway during October 3 8 2010 The 54 revised full papers presented were carefully reviewed and selected from 252 submissions The papers are organized in topical sections on genericity and generalization model migration and incremental manipulation modeling model transformations verifying consistency and conformance taming modeling complexity modeling user system interaction model driven quality assurance managing variability multi modeling approaches distributed embedded software development de composition and refactoring model change meta models at runtime requirements engineering slicing and model transformations incorporating quality concerns in MDD model driven engineering in practice and modeling architecture *The British National Bibliography* Arthur James Wells,2004

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