
System-Level Power-Aware Design Techniques in Real-Time Systems

Osman S. Unsal, Israel Koren, System-Level
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No. 7, July 2003.

Systemlevel Design Techniques For Energyefficient Embedded Systems

Wenjie Huang



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System-Level Design Techniques for Energy-Efficient Embedded Systems Marcus T. Schmitz, Bashir M. Al-Hashimi, Petru Eles, 2006-01-16 System Level Design Techniques for Energy Efficient Embedded Systems addresses the development and validation of co synthesis techniques that allow an effective design of embedded systems with low energy dissipation The book provides an overview of a system level co design flow illustrating through examples how system performance is influenced at various steps of the flow including allocation mapping and scheduling The book places special emphasis upon system level co synthesis techniques for architectures that contain voltage scalable processors which can dynamically trade off between computational performance and power consumption Throughout the book the introduced co synthesis techniques which target both single mode systems and emerging multi mode applications are applied to numerous benchmarks and real life examples including a realistic smart phone

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Energy-Efficient Fault-Tolerant Systems Jimson Mathew, Rishad A. Shafik, Dhiraj K. Pradhan, 2013-09-07 This book describes the state of the art in energy efficient fault tolerant embedded systems It covers the entire product lifecycle of electronic systems design analysis and testing and includes discussion of both circuit and system level approaches Readers will be enabled to meet the conflicting design objectives of energy efficiency and fault tolerance for reliability given the up to date techniques presented

[Sustainable ICTs and Management Systems for Green Computing](#) Hu, Wen-Chen, Kaabouch, Naima, 2012-06-30 This book focuses on information technology using sustainable green computing to reduce energy and resources used Provided by publisher

Advances in Parallel, Distributed Computing Dhinaharan Nagamalai, Eric Renault, Murugan Dhanuskodi, 2011-10-12 This book constitutes the refereed proceedings of the First International Conference on Advances in Parallel Distributed Computing Technologies and Applications PDCTA 2011 held in Tirunelveli India in September 2011 The 64 revised full papers were carefully reviewed and selected from over 400 submissions Providing an excellent international forum for sharing knowledge and results in theory methodology and applications of parallel distributed computing the papers address all current issues in this field with special focus on algorithms and

applications computer networks cyber trust and security wireless networks as well as mobile computing and bioinformatics

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Handbook of Parallel Computing

Sanguthevar Rajasekaran,John Reif,2007-12-20 The ability of parallel computing to process large data sets and handle time consuming operations has resulted in unprecedented advances in biological and scientific computing modeling and simulations Exploring these recent developments the Handbook of Parallel Computing Models Algorithms and Applications provides comprehensive coverage on a

Ambient Intelligence Werner Weber,Jan Rabaey,Emile H.L. Aarts,2005-12-12

Ambient intelligence is the vision of a technology that will become invisibly embedded in our natural surroundings present whenever we need it enabled by simple and effortless interactions attuned to all our senses adaptive to users and context sensitive and autonomous High quality information access and personalized content must be available to everybody anywhere and at any time This book addresses ambient intelligence used to support human contacts and accompany an

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ISLPED'03, 2003 *IEEE Workshop on Signal Processing Systems*, 1997 **Sixth IEEE International High-Level Design Validation and Test Workshop**, 2001. Eighteen full papers and eight short presentations from international engineers and researchers discuss current developments, new methodologies, and emerging trends in design verification and test. The focus of the sixth conference was on hardware/software embedded systems and system-on-chips. Topics include, for example, the automatic validation of pipeline specifications, the use of sequential ATPG for model checking, formal verification of the Pentium 4 multiplier, and using cutwidth to improve symbolic simulation and Boolean satisfiability. The volume is not indexed. c Book News Inc. **18th International Conference on VLSI Design**, International Conference on VLSI Design, 2005 **Design Techniques for Energy-efficient Embedded and Mobile Computing Systems**, Keith S. Vallerio, 2004 **The British National Bibliography**, Arthur James Wells, 2004 **Dissertation Abstracts International**, 2007 **Integrated Circuit and System Design**, 2005 **2007 International Conference on Parallel Processing**, IEEE Staff, 2007 **High Performance Ocean Observing System and Power Saving Technologies**, Shaomin Ding, 2005 **IEEE Circuits & Devices**, 2006 **Advances in Computer Systems Architecture**, 2005

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