

Second Edition

# VLSI FABRICATION PRINCIPLES

SILICON AND GALLIUM ARSENIDE

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*Microelectronic Materials and Processes* Roland Levy, 1989-01-31 The primary thrust of very large scale integration VLSI is the miniaturization of devices to increase packing density achieve higher speed and consume lower power The fabrication of integrated circuits containing in excess of four million components per chip with design rules in the submicron range has now been made possible by the introduction of innovative circuit designs and the development of new microelectronic materials and processes This book addresses the latter challenge by assessing the current status of the science and technology associated with the production of VLSI silicon circuits It represents the cumulative effort of experts from academia and industry who have come together to blend their expertise into a tutorial overview and cohesive update of this rapidly expanding field A balance of fundamental and applied contributions cover the basics of microelectronics materials and process engineering Subjects in materials science include silicon silicides resists dielectrics and interconnect metallization Subjects in process engineering include crystal growth epitaxy oxidation thin film deposition fine line lithography dry etching ion implantation and diffusion Other related topics such as process simulation defects phenomena and diagnostic techniques are also included This book is the result of a NATO sponsored Advanced Study Institute AS held in Castelvecchio Pascoli Italy Invited speakers at this institute provided manuscripts which were edited updated and integrated with other contributions solicited from non participants to this AS *Materials Processing Handbook* Joanna R. Groza, James F. Shackelford, 2007-03-28 The field of materials science and engineering is rapidly evolving into a science of its own While traditional literature in this area often concentrates primarily on property and structure the *Materials Processing Handbook* provides a much needed examination from the materials processing perspective This unique focus reflects the changing complex **Fundamentals of Solid-state Electronics** Chih-Tang Sah, 1993 This companion to *Fundamentals of Solid State Electronics* provides a helpful summary of the main text for students and lecturers alike The clear typeface large font and

point form layout are designed to produce viewgraphs for lectures and to provide ample margins for study notes This Study Guide comes complete with a detailed description of two one semester solid state electronics core courses taught to about 80 100 sophomore junior students each time four years apart It links the contents of the one semester lecture course to the textbook *Fabrication of GaAs Devices* Albert G. Baca, Carol I.H. Ashby, Institution of Electrical Engineers, 2005-09 This book provides fundamental and practical information on all aspects of GaAs processing and gives pragmatic advice on cleaning and passivation wet and dry etching and photolithography Other topics covered include device performance for HBTs Heterojunction Bipolar Transistors and FETs Field Effect Transistors how these relate to processing choices and special processing issues such as wet oxidation which are especially important in optoelectronic devices This book is suitable for both new and practising engineers

**Principles of Growth and Processing of Semiconductors** Subhash Mahajan, K. S. SreeHarsha, 1999 Developing the essential elements of semiconductor behaviour this text goes on to provide a conceptual framework and introduction to microelectronics Topics include semiconductors devices defects evaluation bulk growth epitaxial growth oxidation diffusion and ion implantation

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**Solid State Devices and Technology** Babu V Suresh, 2010-09

*A Guide to the Literature of Electrical and Electronics Engineering* Susan Ardis, 1987

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