Sync vs Async Communication

Request and wait for a response
Fire and forget with messages/events

Synchronous

Request-response model

Send a request and wait for some response
Example: API requests

Asynchronous

Fire and forget model

Send message/event and forget
Example: Event Driven Architecture

Advanced Async Communication

JA Banks

Advanced Async Communication:

Recent Advances in the Message Passing Interface Claudia Blaas-Schenner,Christoph Niethammer,Tobias Haas,2024-09-24 This volume LNCS constitutes the refereed proceedings of 31st European MPI Users Group Meeting EuroMPI 2024 held in Perth WA Australia during September 25 27 2024 The 8 full papers presented here were carefully reviewed and selected from 19 submissions These papers have been categorized under the following topical sections Compile time Correctness checks and optimization Limitations and Extensions for GPGPUs in MPI Improvements for MPI and MPI Ecosystem **Low-Power Electronics Design** Christian Piguet,2018-10-03 The power consumption of integrated circuits is one of the most problematic considerations affecting the design of high performance chips and portable devices The study of power saving design methodologies now must also include subjects such as systems on chips embedded software and the future of microelectronics Low Power Electronics Design covers all major aspects of low power design of ICs in deep submicron technologies and addresses emerging topics related to future design This volume explores in individual chapters written by expert authors the many low power techniques born during the past decade It also discusses the many different domains and disciplines that impact power consumption including processors complex circuits software CAD tools and energy sources and management The authors delve into what many specialists predict about the future by presenting techniques that are promising but are not yet reality They investigate nanotechnologies optical circuits ad hoc networks e textiles as well as human powered sources of energy Low Power Electronics Design delivers a complete picture of today s methods for reducing power and also illustrates the advances in chip design that may be commonplace 10 or 15 years from now   Low-Power Processors and Systems on Chips Christian Piguet,2018-10-03 The power consumption of microprocessors is one of the most important challenges of high performance chips and portable devices In chapters drawn from Piguet s recently published Low Power Electronics Design this volume addresses the design of low power microprocessors in deep submicron technologies It provides a focused reference for specialists involved in systems on chips from low power microprocessors to DSP cores reconfigurable processors memories ad hoc networks and embedded software Low Power Processors and Systems on Chips is organized into three broad sections for convenient access The first section examines the design of digital signal processors for embedded applications and techniques for reducing dynamic and static power at the electrical and system levels The second part describes several aspects of low power systems on chips including hardware and embedded software aspects efficient data storage networks on chips and applications such as routing strategies in wireless RF sensing and actuating devices The final section discusses embedded software issues including details on compilers retargetable compilers and coverification tools Providing detailed examinations contributed by leading experts Low Power Processors and Systems on Chips supplies authoritative information on how to maintain high performance while lowering power consumption in modern processors and SoCs It is a must read for anyone designing

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