

VLSI SIGNAL PROCESSING, III

Edited by
Robert W. Brodersen
Howard S. Moscovitz



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VLSI Signal Processing, II Sun Yuan Kung, Robert E. Owen, J. Greg Nash, 1986

Computer Systems and Software

Engineering Patrick DeWilde, Joos P.L. Vandewalle, 2012-12-06 Computer Systems and Software Engineering is a compilation of sixteen state of the art lectures and keynote speeches given at the COMPEURO 92 conference The contributions are from leading researchers each of whom gives a new insight into subjects ranging from hardware design through parallelism to computer applications The pragmatic flavour of the contributions makes the book a valuable asset for both researchers and designers alike The book covers the following subjects Hardware Design memory technology logic design algorithms and architecture Parallel Processing programming cellular neural networks and load balancing Software Engineering machine learning logic programming and program correctness Visualization the graphical computer interface

ICASSP 88: D, digital signal processing, 1988

Specification And Verification Of Systolic Arrays Magdy A

Bayoumi, Nam Ling, 1999-08-05 Circuits and architectures have become more complex in terms of structure interconnection topology and data flow Design correctness has become increasingly significant as errors in design may result in strenuous debugging or even in the repetition of a costly manufacturing process Although circuit simulation has been used traditionally and widely as the technique for checking hardware and architectural designs it does not guarantee the conformity of designs to specifications Formal methods therefore become vital in guaranteeing the correctness of designs and have thus received a significant amount of attention in the CAD industry today This book presents a formal method for specifying and verifying the correctness of systolic array designs Such architectures are commonly found in the form of accelerators for digital signal image and video processing These arrays can be quite complicated in topology and data flow In the book a formalism called STA is defined for these kinds of dynamic environments with a survey of related techniques A framework for specification and verification is established Formal verification techniques to check the correctness of the systolic networks with respect to the algorithmic level specifications are explained The book also presents a Prolog based formal design verifier named VSTA developed to automate the verification process as using a general purpose theorem prover is usually extremely time consuming Several application examples are included in the book to illustrate how formal techniques and the verifier can be used to automate proofs

Readings in Hardware/Software Co-Design Giovanni De Micheli, Rolf Ernst, Wayne Wolf, 2002

This title serves as an introduction and reference for the field with the papers that have shaped the hardware software co design since its inception in the early 90s

VLSI Signal Processing Technology Magdy Bayoumi, Earl

Swartzlander, 1994-10-31 This book is the first in a set of forthcoming books focussed on state of the art development in the VLSI Signal Processing area It is a response to the tremendous research activities taking place in that field These activities have been driven by two factors the dramatic increase in demand for high speed signal processing especially in consumer

electronics and the evolving microelectronic technologies The available technology has always been one of the main factors in determining algorithms architectures and design strategies to be followed With every new technology signal processing systems go through many changes in concepts design methods and implementation The goal of this book is to introduce the reader to the main features of VLSI Signal Processing and the ongoing developments in this area The focus of this book is on Current developments in Digital Signal Processing DSP processors and architectures several examples and case studies of existing DSP chips are discussed in Chapter 1 Features and requirements of image and video signal processing architectures both applications specific integrated circuits ASICs and programmable image processors are studied in Chapter 2 New market areas for signal processing especially in consumer electronics such as multimedia teleconferencing and movie on demand Impact of arithmetic circuitry on the performance of DSP processors several topics are discussed in Chapter 3 such as number representation arithmetic algorithms and circuits and implementation Research in Progress ,1982

Research in Progress United States. Army Research Office,1989 Vols for 1977 consist of two parts Chemistry biological sciences engineering sciences metallurgy and materials science issued in the spring and Physics electronics mathematics geosciences issued in the fall *Signal Processing III* Ian T. Young,1986 Very Good No Highlights or Markup all pages are intact Advanced Signal-processing Algorithms, Architectures, and Implementations ,1991 *Communication, Control, and Signal Processing* Erdal Arıkan,1990 The articles in this two volume set provide a wealth of knowledge on new and emerging trends and topics in three areas of Electrical Engineering Communication Control and Signal Processing Twenty invited speakers and 442 authors from 35 countries contributed to this work resulting in 240 very valuable papers Containing contributions by some of the world's leading scientists the two volumes provide the definitive reference source for anyone interested in the latest advances and results of current fundamental research in these fields **Electrical & Electronics Abstracts** ,1993 **ICASSP 88: A & U, audio & electroacoustics, underwater signal processing** ,1988 VLSI Signal Processing, VI ,2002 **Digital Signal Processing Applications** ,1997 Proceedings of Euro ASIC '90 Institut national polytechnique (Grenoble). Club conception de circuits à la demande,1990 **IEEE Proceedings of the Southeastcon** ,1988 **Euro ASIC** ,1990

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