
VLSI-COMPATIBLE IMPLEMENTATIONS FOR ARTIFICIAL NEURAL NETWORKS

by

**Sied Mehdi Fakhraie
Kenneth C. Smith**

Springer Science+Business Media, LLC

Vlsi Compatible Implementations For Artificial Neural Networks

Jan Crols, Michiel Steyaert



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VLSI-compatible Implementations for Artificial Neural Networks [microform] Sied Mehdi Fakhraie,1995 VLSI -
Compatible Implementations for Artificial Neural Networks Sied Mehdi Fakhraie,Kenneth C Smith,1996-12-01

Switched-Current Design and Implementation of Oversampling A/D Converters Nianxiong Tan,2012-12-06

Switched Current Design and Implementation of Oversampling A D Converters discusses the switched current SI technique and its application in oversampling A D converters design The SI technique is an analog sampled data technique that fully exploits the digital CMOS process Compared with the traditional switched capacitor SC technique the SI technique has both pros and cons that are highlighted in the book With the consideration of similarity and difference of SI and SC techniques oversampling A D converter architectures are tailored and optimized for SI design and implementation in the book Switched Current Design and Implementation of Oversampling A D Converters emphasizes the practical aspects of SI circuits without tedious mathematical derivations and is full of circuit design and implementation examples There are more than 10 different chips included in the book demonstrating the high speed over 100 MHz and ultra low voltage 1.2 V operation of SI circuits and systems in standard digital CMOS processes Therefore the book is of special value as a practical guide for designing SI circuits and SI oversampling A D converters Switched Current Design and Implementation of Oversampling A D Converters serves as an excellent reference for analog designers especially A D converter designers and is of interest to digital designers for real time signal processing who need A D interfaces The book may also be used as a text for advanced courses on the subject

Design of Modulators for Oversampled Converters Feng Wang,Ramesh Harjani,2012-12-06 Oversampled A D converters have become very popular in recent years Some of their advantages include relaxed requirements for anti alias filters relaxed requirements for component matching high resolution and compatibility with digital VLSI technology There is a significant amount of literature discussing the principle theory and implementation of various oversampled converters Such converters are likely to continue to proliferate in the foreseeable future Additionally more recently there has been great interest in low voltage and low power circuit design New design techniques have been proposed for both the digital domain and the analog domain Both trends point to the importance of the low power design of oversampled A D converters Unfortunately there has been no systematic study of the optimal design of modulators for oversampled converters Design has generally focused on new architectures with little attention being paid to optimization The goal of Design of Modulators for Oversampled Converters is to develop a methodology for the optimal design of modulators in oversampled converters The primary focus of the presentation is on minimizing power consumption and understanding and limiting the nonlinearities that result in such converters Design of Modulators for Oversampled Converters offers a quantitative justification for the various design tradeoffs and serves as a guide for designing low power highly linear oversampled converters Design of Modulators for Oversampled Converters will serve as a valuable guide for circuit design practitioners university researchers and

graduate students who are interested in this fast moving area

Symbolic Analysis in Analog Integrated Circuit

Design Henrik Floberg, 2012-12-06 Symbolic Analysis in Analog Integrated Circuit Design provides an introduction to computer aided circuit analysis and presents systematic methods for solving linear i e small signal and nonlinear circuit problems which are illustrated by concrete examples Computer aided symbolic circuit analysis is useful in analog integrated circuit design Analytic expressions for the network transfer functions contain information that is not provided by a numerical simulation result However these expressions are generally extremely long and difficult to interpret therefore it is necessary to be able to approximate them guided by the magnitude of the individual circuit parameters Engineering has been described as the art of making approximations The inclusion of symbolic analysis in analog circuit design reduces the implied risk of ambiguity during the approximation process A systematic method based on the nullor concept is used to obtain the basic feedback transistor amplifier configurations Approximate expressions for the locations of poles and zeros for linear networks are obtained using the extended pole splitting technique An unusual feature in Symbolic Analysis in Analog Integrated Circuit Design is the consistent use of the transadmittance element with finite linear or nonlinear or infinite i e nullor gain as the only requisite circuit element The describing function method is used to obtain approximate symbolic expressions for the harmonic distortion generated by a soft or hard transconductance nonlinearity embedded in an arbitrary linear network The design and implementation of a program i e CASCA for symbolic analysis of time continuous networks is described The algorithms can also be used to solve other linear problems e g the analysis of time discrete switched capacitor networks Symbolic Analysis in Analog Integrated Circuit Design serves as an excellent resource for students and researchers as well as for industry designers who want to familiarize themselves with circuit analysis This book may also be used for advanced courses on the subject

CMOS Wireless Transceiver Design Jan Crols, Michiel Steyaert, 2013-06-29 The world of wireless communications is changing very rapidly since a few years The introduction of digital data communication in combination with digital signal processing has created the foundation for the development of many new wireless applications High quality digital wireless networks for voice communication with global and local coverage like the GSM and DECT system are only faint and early examples of the wide variety of wireless applications that will become available in the remainder of this decade The new evolutions in wireless communications set new requirements for the transceivers transmitter receivers Higher operating frequencies a lower power consumption and a very high degree of integration are new specifications which ask for design approaches quite different from the classical RF design techniques The integratability and power consumption reduction of the digital part will further improve with the continued downscaling of technologies This is however completely different for the analog transceiver front end the part which performs the interfacing between the antenna and the digital signal processing The analog front end's integratability and power consumption are closely related to the physical limitations of the transceiver topology and not so much to the scaling of the used technology Chapter 2 gives a

detailed study of the level of integration in current transceiver realization and analyzes their limitations In chapter 3 of this book the complex signal technique for the analysis and synthesis of multi path receiver and transmitter topologies is introduced

Design of Low-Voltage Low-Power CMOS Delta-Sigma A/D Converters Vincenzo Peluso, Michiel Steyaert, Willy M.C. Sansen, 2013-03-09 Design of Low Voltage Low Power CMOS Delta Sigma A D Converters investigates the feasibility of designing Delta Sigma Analog to Digital Converters for very low supply voltage lower than 1.5V and low power operation in standard CMOS processes The chosen technique of implementation is the Switched Opamp Technique which provides Switched Capacitor operation at low supply voltage without the need to apply voltage multipliers or low V_t MOST devices A method of implementing the classic single loop and cascaded Delta Sigma modulator topologies with half delay integrators is presented Those topologies are studied in order to find the parameters that maximise the performance in terms of peak SNR Based on a linear model the performance degradations of higher order single loop and cascaded modulators compared to a hypothetical ideal modulator are quantified An overview of low voltage Switched Capacitor design techniques such as the use of voltage multipliers low V_t MOST devices and the Switched Opamp Technique is given An in depth discussion of the present status of the Switched Opamp Technique covers the single ended Original Switched Opamp Technique the Modified Switched Opamp Technique which allows lower supply voltage operation and differential implementation including common mode control techniques The restrictions imposed on the analog circuits by low supply voltage operation are investigated Several low voltage circuit building blocks some of which are new are discussed A new low voltage class AB OTA especially suited for differential Switched Opamp applications together with a common mode feedback amplifier and a comparator are presented and analyzed As part of a systematic top down design approach the non ideal charge transfer of the Switched Opamp integrator cell is modeled based upon several models of the main opamp non ideal characteristics Behavioral simulations carried out with these models yield the required opamp specifications that ensure that the intended performance is met in an implementation A power consumption analysis is performed The influence of all design parameters especially the low power supply voltage is highlighted Design guidelines towards low power operation are distilled Two implementations are presented together with measurement results The first one is a single ended implementation of a Delta Sigma ADC operating with 1.5V supply voltage and consuming 100 μ W for a 74 dB dynamic range in a 3.4 kHz bandwidth The second implementation is differential and operates with 900 mV It achieves 77 dB dynamic range in 16 kHz bandwidth and consumes 40 μ W Design of Low Voltage Low Power CMOS Delta Sigma A D Converters is essential reading for analog design engineers and researchers

Knowledge-Based Intelligent Information and Engineering Systems Bruno Apolloni, 2007-09-12 This book is part of a three volume set that constitutes the refereed proceedings of the 11th International Conference on Knowledge Based Intelligent Information and Engineering Systems KES 2007 Coverage in this first volume includes artificial neural networks and connectionists systems fuzzy and neuro fuzzy systems evolutionary computation machine learning and

classical AI agent systems and information engineering and applications in ubiquitous computing environments Compact Low-Voltage and High-Speed CMOS, BiCMOS and Bipolar Operational Amplifiers Klaas-Jan de Langen, Johan Huijsing, 2013-03-14 Compact Low Voltage and High Speed CMOS BiCMOS and Bipolar Operational Amplifiers discusses the design of integrated operational amplifiers that approach the limits of low supply voltage or very high bandwidth The resulting realizations span the whole field of applications from micro power CMOS VLSI amplifiers to 1 GHz bipolar amplifiers The book presents efficient circuit topologies in order to combine high performance with simple solutions In total twelve amplifier realizations are discussed Two bipolar amplifiers are discussed a 1 GHz operational amplifier and an amplifier with a high ratio between the maximum output current and the quiescent current Five amplifiers have been designed in CMOS technology extremely compact circuits that can operate on supply voltages down to one gate source voltage and two saturation voltages which equals about 1.4 V and ultimate low voltage amplifiers that can operate on supply voltages down to one gate source voltage and one saturation voltage which amounts to about 1.2 V In BiCMOS technology five amplifiers have been designed The first two amplifiers are based on a compact topology Two other amplifiers are designed to operate on low supply voltages down to 1.3 V The final amplifier has a unity gain frequency of 200 MHz and can operate down to 2.5 V Compact Low Voltage and High Speed CMOS BiCMOS and Bipolar Operational Amplifiers is intended for the professional analog designer Also it is suitable as a text book for advanced courses in amplifier design **Silicon Implementation of Pulse Coded Neural Networks** Mona E. Zaghoul, Jack L. Meador, Robert W. Newcomb, 1994-03-31 When confronted with the hows and whys of nature's computational engines some prefer to focus upon neural function addressing issues of neural system behavior and its relation to natural intelligence Then there are those who prefer the study of the mechanics of neural systems the nuts and bolts of the wetware the neurons and synapses Those who investigate pulse coded implementations of artificial neural networks know what it means to stand at the boundary which lies between these two worlds not just asking why natural neural systems behave as they do but also how they achieve their marvelous feats The research results presented in this book not only address more conventional abstract notions of neural like processing but also the more specific details of neural like processors It has been established for some time that natural neural systems perform a great deal of information processing via electrochemical pulses Accordingly pulse coded neural network concepts are receiving increased attention in artificial neural network research This increased interest is compounded by continuing advances in the field of VLSI circuit design This is the first time in history in which it is practical to construct networks of neuron like circuits of reasonable complexity that can be applied to real problems We believe that the pioneering work in artificial neural systems presented in this book will lead to further advances that will not only be useful in some practical sense but may also provide some additional insight into the operation of their natural counterparts Stochastic Models of Neural Networks Claudio Turchetti, 2004 IEEE Transactions on Circuits and Systems , 2006 **Parallel Architectures**

and Neural Networks Eduardo R. Caianiello, 1990 Papers presented at the Third Workshop on Parallel Architectures and Neural Networks organized by the International Institute for Advanced Scientific Studies in collaboration with other Italian institutions

The Design of Low-Voltage, Low-Power Sigma-Delta Modulators Shahriar Rabii, Bruce A. Wooley, 1999

Oversampling techniques based on sigma delta modulation are widely used to implement the analog digital interfaces in CMOS VLSI technologies. This approach is relatively insensitive to imperfections in the manufacturing process and offers numerous advantages for the realization of high resolution analog to digital A/D converters in the low voltage environment that is increasingly demanded by advanced VLSI technologies and by portable electronic systems. In *The Design of Low Voltage Low Power Sigma Delta Modulators* an analysis of power dissipation in sigma delta modulators is presented and a low voltage implementation of a digital audio performance A/D converter based on the results of this analysis is described. Although significant power savings can typically be achieved in digital circuits by reducing the power supply voltage the power dissipation in analog circuits actually tends to increase with decreasing supply voltages. Oversampling architectures are a potentially power efficient means of implementing high resolution A/D converters because they reduce the number and complexity of the analog circuits in comparison with Nyquist rate converters. In fact it is shown that the power dissipation of a sigma delta modulator can approach that of a single integrator with the resolution and bandwidth required for a given application. In this research the influence of various parameters on the power dissipation of the modulator has been evaluated and strategies for the design of a power efficient implementation have been identified. *The Design of Low Voltage Low Power Sigma Delta Modulators* begins with an overview of A/D conversion emphasizing sigma delta modulators. It includes a detailed analysis of noise in sigma delta modulators, analyzes power dissipation in integrator circuits and addresses practical issues in the circuit design and testing of a high resolution modulator. *The Design of Low Voltage Low Power Sigma Delta Modulators* will be of interest to practicing engineers and researchers in the areas of mixed signal and analog integrated circuit design.

Proceedings of IEEE International Conference on Industrial Technology 2000, 2000

IEICE Transactions on Communications, Electronics, Information, and Systems, 1991

DARPA Neural Network Study Final Report DARPA Neural Network Study (U.S.), 1989

Proceedings of the International Conference on Electromagnetic Interference and Compatibility, 2003

Smart Engineering System Design Cihan H. Dagli, 1999

Proceedings of the Artificial Neural Networks in Engineering Conference November 5-8 2000 St. Louis Missouri. The 179 papers compiled in this book focus on building smart components to engineering systems currently available. Topics discussed include Neural Networks, Fuzzy Systems, Complex Systems, Pattern Recognition, Smart Engineering Systems, Evolutionary Programming, Data Mining, Adaptive Control and Biology and Medicine. Special tenth anniversary edition includes subject and author indices.

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