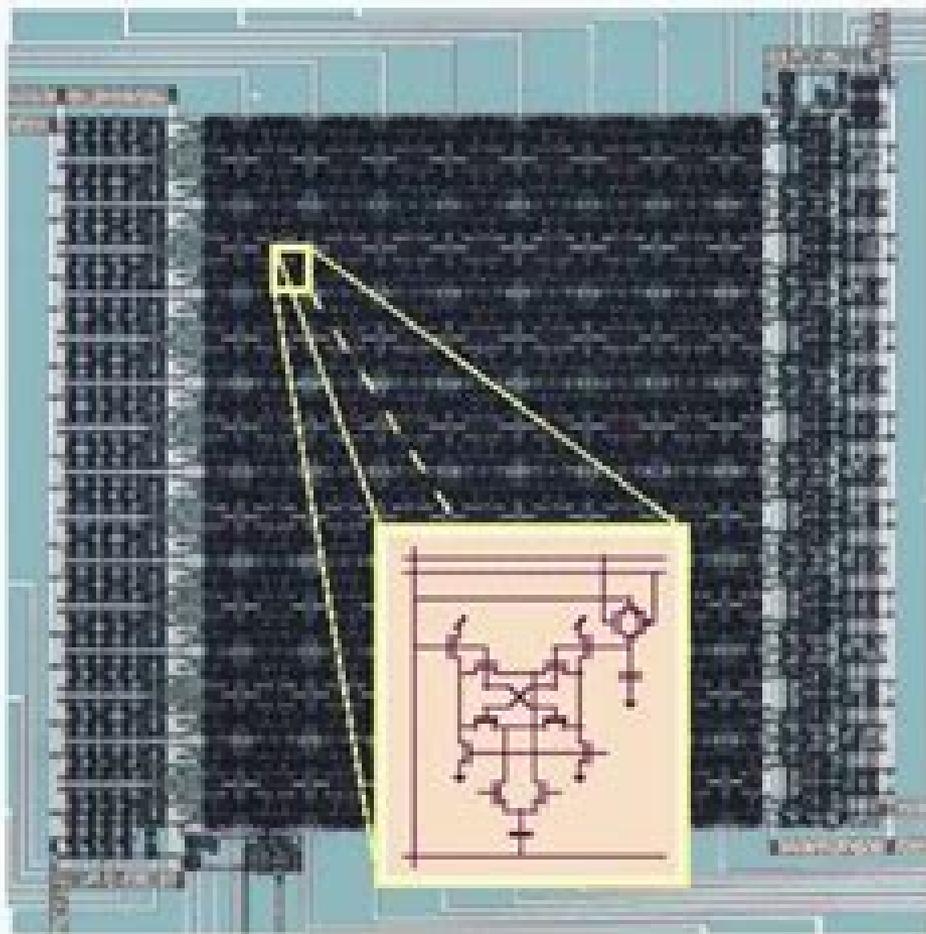


# Massively Parallel Distributed VLSI Computation



- **Neuromorphic**
  - distributed representation
  - local memory and adaptation
  - sensory interface
  - physical computation
  - internally analog, externally digital
- **Scalable**
  - throughput scales linearly with silicon area
- **Ultra Low-Power**
  - factor 100 to 10,000 less energy than CPU or DSP

*Example: VLSI Analog-to-digital vector quantizer (Cauwenberghs and Pedroni, 1997)*

# Vlsi And Parallel Computation

**Peter Kinget, Michiel Steyaert**



## **Vlsi And Parallel Computation:**

Vlsi And Parallel Computing For Pattern Recognition And Artificial Intelligence N Ranganathan,1995-06-30 This book covers parallel algorithms and architectures and VLSI chips for a range of problems in image processing computer vision pattern recognition and artificial intelligence The specific problems addressed include vision and image processing tasks Fast Fourier Transforms Hough Transforms Discrete Cosine Transforms image compression polygon matching template matching pattern matching fuzzy expert systems and image rotation The collection of papers gives the reader a good introduction to the state of the art while for an expert this serves as a good reference and a source of some new contributions in this field

VLSI and Parallel Computation Robert Suaya,Graham M. Birtwistle,1990 This book deals with issues from the world of highly parallel systems containing hundreds of thousands of processors      **Algorithms and Parallel VLSI Architectures III** M. Moonen,F. Catthoor,1995-03-16 A comprehensive overview of the current evolution of research in algorithms architectures and compilation for parallel systems is provided by this publication The contributions focus specifically on domains where embedded systems are required either oriented to application specific or to programmable realisations These are crucial in domains such as audio telecom instrumentation speech robotics medical and automotive processing image and video processing TV multimedia radar and sonar The book will be of particular interest to the academic community because of the detailed descriptions of research results presented In addition many contributions feature the real life applications that are responsible for driving research and the impact of their specific characteristics on the methodologies is assessed The publication will also be of considerable value to senior design engineers and CAD managers in the industrial arena who wish either to anticipate the evolution of commercially available design tools or to utilize the presented concepts in their own R D programmes      **Parallel Algorithms for VLSI Computer-aided Design** Prithviraj Banerjee,1994-01-01 This text discusses the design and use of practical parallel algorithms for solving problems in a growing application area whose computational requirements are enormous VLSI CAD applications      **Introduction to Parallel Processing** Bruno Codenotti,Mauro Leoncini,1993-01 This book provides programmers with a unique insight into the rapidly expanding field of parallel processing It contains a thorough discussion of the basic concepts of the theory of algorithms and complexity necessary to grasp the foundations of parallelism Provides professionals with a focused discussion of VLSI design and balanced coverage of hardware software and theory      **VLSI & Parallel Computing for Pattern Recognition & Artificial Intelligence** N. Ranganathan,1995      **Parallel Algorithms for VLSI Computer-aided Design Applications** Prithviraj Banerjee,1994 Parallel computing is becoming an increasingly cost effective and affordable means for providing enormous computing power While it is relatively easy to build massively parallel systems it is not always easy to harness their power effectively This book was written to help readers design good parallel algorithms that can efficiently use hardware resources to get maximum performance      **VLSI Systems and Computations** H.T. Kung,R. Sproull,G. Steele,2012-12-06

The papers in this book were presented at the CMU Conference on VLSI Systems and Computations held October 19-21, 1981 in Pittsburgh, Pennsylvania. The conference was organized by the Computer Science Department, Carnegie Mellon University, and was partially supported by the National Science Foundation and the Office of Naval Research. These proceedings focus on the theory and design of computational systems using VLSI. Until very recently, integrated circuit research and development were concentrated in the device physics and fabrication design disciplines, and in the integrated circuit industry itself. Within the last few years, a community of researchers is growing to address issues closer to computer science: the relationship between computing structures and the physical structures that implement them; the specification and verification of computational processes implemented in VLSI; the use of massively parallel computing made possible by VLSI; the design of special purpose computing architectures; and the changes in general purpose computer architecture that VLSI makes possible. It is likely that the future exploitation of VLSI technology depends as much on structural and design innovations as on advances in fabrication technology.

The book is divided into nine sections:

- Invited Papers:** Six distinguished researchers from industry and academia presented invited papers.
- Models of Computation:** The papers in this section deal with abstracting the properties of VLSI circuits into models that can be used to analyze the chip area, time, or energy required for a particular computation.
  - Systolic Computations M.A. Frumkin, 2012-12-06: This monograph is devoted to a new method of parallel computing which uses VLSI technology in an efficient manner. By this method, data are fed to the cells of a systolic processor and results are obtained instantly. Some theoretical and algorithmic questions which arise in the design of hardware and software for systolic processing are considered. Special attention is devoted to the complexity of VLSI algorithms, parallel algorithms, relations between graphs of algorithms and graphs of processors, parallel programming languages, and the use of systolic algorithms for vector programming. The book is unique for its inclusion of a library of systolic algorithms for solving problems from twelve branches of computer science and will be useful for designers of hardware and software for parallel processing.
    - Parallel Algorithms and Architectures for DSP Applications* Magdy A. Bayoumi, 2012-12-06: Over the past few years, the demand for high speed Digital Signal Processing (DSP) has increased dramatically. New applications in real time image processing, satellite communications, radar signal processing, pattern recognition, and real time signal detection and estimation require major improvements at several levels: algorithmic, architectural, and implementation. These performance requirements can be achieved by employing parallel processing at all levels. Very Large Scale Integration (VLSI) technology supports and provides a good avenue for parallelism. Parallelism offers efficient solutions to several problems which can arise in VLSI DSP architectures, such as:
      1. Intermediate data communication and routing: several DSP algorithms such as FFT involve excessive data routing and reordering. Parallelism is an efficient mechanism to minimize the silicon cost and speed up the processing time of the intermediate middle stages.
      2. Complex DSP applications: the required computation is almost doubled. Parallelism will allow two similar channels processing at the same

time The communication between the two channels has to be minimized 3 Application specific systems this emerging approach should achieve real time performance in a cost effective way 4 Testability and fault tolerance reliability has become a required feature in most of DSP systems To achieve such property the involved time overhead is significant Parallelism may be the solution to maintain acceptable speed performance

**VLSI** Fabrizio Luccio,1985 [Parallel Processing on VLSI Arrays](#) Josef A. Nossek,2012-12-06 Guest Editor JOSEF A NOSSEK This is a special issue of the Journal of VLSI Signal Processing comprising eight contributions invited for publication on the basis of novel work presented in a special session on Parallel Processing on VLSI Arrays at the International Symposium on Circuits and Systems ISCAS held in New Orleans in May 1990 Massive parallelism to cope with high speed requirements stemming from real time applications and the restrictions in architectural and circuit design such as regularity and local connectedness brought about by the VLSI technology are the key questions addressed in these eight papers They can be grouped into three subsections elaborating on Simulation of continuous physical systems i.e numerically solving partial differential equations Neural architectures for image processing and pattern recognition Systolic architectures for implementing regular and irregular algorithms in VLSI technology The paper by A Fettweis and O Nitsche advocates a signal processing approach for the numerical integration of partial differential equations PDEs It is based on the principles of multidimensional wave digital filters MDWDFs thereby preserving the passivity of energy dissipating physical systems It is particularly suited for systems of PDEs involving time and finite propagation speed The basic ideas are explained using Maxwell's equations as a vehicle for the derivation of a multidimensional equivalent circuit representing the spatially infinitely extended arrangement with only very few circuit elements

[Efficient interconnection schemes for VLSI and parallel computation](#) Ronald I. Greenberg,1989 [Analog VLSI Integration of Massive Parallel Signal Processing Systems](#) Peter Kinget,Michiel Steyaert,2013-06-29 When comparing conventional computing architectures to the architectures of biological neural systems we find several striking differences Conventional computers use a low number of high performance computing elements that are programmed with algorithms to perform tasks in a time sequenced way they are very successful in administrative applications in scientific simulations and in certain signal processing applications However the biological systems still significantly outperform conventional computers in perception tasks sensory data processing and motory control Biological systems use a completely different computing paradigm a massive network of simple processors that are adaptively interconnected and operate in parallel Exactly this massively parallel processing seems the key aspect to their success On the other hand the development of VLSI technologies provide us with technological means to implement very complicated systems on a silicon die Especially analog VLSI circuits in standard digital technologies open the way for the implementation of massively parallel analog signal processing systems for sensory signal processing applications and for perception tasks In chapter 1 the motivations behind the emergence of the analog VLSI of massively parallel systems is discussed in detail together with the capabilities and imitations of VLSI

technologies and the required research and developments Analog parallel signal processing drives for the development of very compact high speed and low power circuits An important technological limitation in the reduction of the size of circuits and the improvement of the speed and power consumption performance is the device inaccuracies or device mismatch

**Parallel Processing Architectures and VLSI Hardware** Angel L. DeCegama,1989      **Parallel Methods for VLSI Layout Design** Si. Pi Ravikumār,1996      VLSI Design, Parallel Computation and Distributed Computing ,1991 The research efforts reported here have centered in the areas of parallel and distributed computing network architecture combinatorial algorithms and complexity theory Significant progress has been made on the development of efficient sorting circuits network management protocols for high speed networks distributed graph algorithms and data structures improved algorithms for packet routing and sorting in parallel machines algorithms for reconfiguring networks around faults improved approximation algorithms for a variety of NP hard optimization problems VLSI design and algorithms for combinatorial problems such as multicommodity flow      *VLSI* Paolo Bertolazzi,Fabrizio Luccio,1985 Good No Highlights No Markup all pages are intact Slight Shelfwear may have the corners slightly dented may have slight color changes slightly damaged spine  
*Parallel Computation of Non-deterministic Algorithms in VLSI* Peter Dirk Hortensius,1985      **VLSI Systems for Analog and Hamming Parallel Computation** Volnei A. Pedroni,Amnon Yariv,California Institute of Technology. Division of Engineering and Applied Science,1995

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