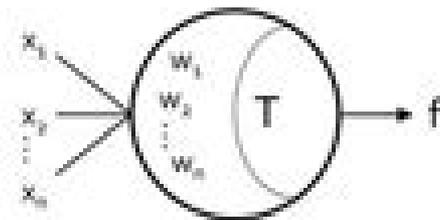


# Threshold Logic (1/2)

- A threshold function  $f$  is a multi-input function defined as below:

$$y = \begin{cases} 1 & \text{if } \sum_{i=1}^n x_i w_i \geq T + \delta_{on} \\ 0 & \text{if } \sum_{i=1}^n x_i w_i < T - \delta_{off} \end{cases}$$



$n$  binary inputs  $x_1, x_2, \dots, x_n$  with weights  $w_1, w_2, \dots, w_n$

a single binary output  $y$

a threshold value  $T$

$\delta_{on}$  and  $\delta_{off}$  represent the defect tolerance

$\delta_{on}$  and  $\delta_{off}$  are both assumed to be zero in much research

# Threshold Logic

**H. F. Klock, R. D. Haney, CASE INST OF  
TECH CLEVELAND OHIO.**

## **Threshold Logic:**

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**LOGIC OF CONTROLLED THRESHOLD DEVICES.** H. F. Klock,R. D. Haney,CASE INST OF TECH CLEVELAND OHIO.,1965 The synthesis of threshold logic circuits from several points of view is presented The first approach is applicable to resistor transistor networks in which the outputs are tied to a common collector resistor In general fewer threshold logic gates than NOR gates connected to a common collector resistor are required Synthesis techniques based upon the use of Boolean matrices are presented In this case the goal of the synthesis is a network to realize a specified function such that the failure of any but the output gate can be compensated for by a change in the threshold level and possibly changes in the weights of the inputs In general the ability to compensate for a failure requires the synthesis of a network with more gates than if no compensation were required The net result is that the synthesis is an involved procedure A mechanism for changing weights of threshold gates using a photo diode matrix is presented The design of the control system for an IBM type 350 disc file is presented as an illustration of the design of a practical system in which threshold logic is employed The function of the control system is to position the read write heads over the specified track of a specified disc to read in or to read out data Less logic circuitry is required if threshold logic rather than NOR logic is used Author

**Threshold Logic Functions** Chuen Kit Yue,1963      *THRESHOLD LOGIC.* Kenneth R. KAPLAN,Robert O. Winder,RCA LABS PRINCETON N J.,1965 The report has four independent sections 1 Test Synthesis Evaluation reports a statistical study of a family of linear programming test synthesis procedures a best method is definitely established 2 Symmetry Types for Threshold Logic is a tutorial exposition including a careful development of the Goto Takahasi self dual type ideas 3 Best Threshold Gate Decisions reports a comparison on the 2470 7 argument threshold function representatives of seven different methods of approximating weights on the basis of Chow parameters the earlier work with a geometric interpretation is shown best 4 Threshold Gate Networks reviews the previously discussed 2 algorithm in geometric terms describes our FORTRAN implementation of it and reports an experiment which suggests that only one in fifteen five argument functions is 2 realizable Author

**Computational Intelligence** Rudolf Kruse,Christian Borgelt,Christian Braune,Sanz Mostaghim,Matthias Steinbrecher,2016-09-16 This textbook provides a clear and logical introduction to the field covering the fundamental concepts algorithms and practical implementations behind efforts to develop systems that exhibit intelligent behavior in complex environments This enhanced second edition has been fully revised and expanded with new content on swarm intelligence deep learning fuzzy data analysis and discrete decision graphs Features provides supplementary material

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*Threshold Logic* Philip M. Lewis, Clarence Leroy Coates, 1967

MORE ABOUT THRESHOLD LOGIC. R. O. WINDER, Radio Corporation of America. David Sarnoff Research Center, Princeton, N.J., 1961 Ideas are discussed on Switching Theory and Logical Design The main subject is composed synthesis The family of functions of  $n$  arguments realizable in a single stage becomes a vanishing fraction of all switching functions of  $n$  arguments as  $n$  grows for  $n$  equals 7 the ratio is ABOUT 10 TO THE 28 1 2 POWER An algorithm is provided for determining 2 realizability realizability with two threshold elements The approach produces a good solution in any case but one guaranteed optimal only for 2 realizable functions A geometric terminology is used this new language is also used where higher necessary conditions for realizability are discussed A CONJECTURE THAT CERTAIN OF THESE CONDITIONS MIGHT BE SUFFICIENT IS DISPROVED THREE RELATED CONDITIONS ARE TREATED IN A COMMON LANGUAGE Optimal integral single stage realizations are considered A conjecture that such a realization gives equal arguments equal weights is disproved Author

Artificial Neural Nets. Problem Solving Methods José Mira, José R. Alvarez, 2003-08-03 The two volume set LNCS 2686 and LNCS 2687 constitute the refereed proceedings of the 7th International Work Conference on Artificial and Natural Neural Networks IWANN 2003 held in Mallorca Menorca Spain in June 2003 The 197 revised papers presented were carefully reviewed and selected for inclusion in the book and address the following topics mathematical and computational methods in neural modelling neurophysiological data analysis and modelling structural and functional models of neurons learning and other plasticity phenomena complex systems dynamics cognitive processes and artificial intelligence methodologies for net design bio inspired systems and engineering and applications in a broad variety of fields

**NBS Special Publication**, 1968

**Threshold Logic** Qinglai Sheng, 1969

**Fundamentals of Threshold Logic** Robert O. Winder, RCA LABS PRINCETON NJ., Radio Corporation of America. RCA Laboratories, Air Force Cambridge Research Laboratories (U.S.), 1968 These notes on threshold logic are intended as intermediary material between a completely geometric heuristic presentation and the more formal source material available in the literature Basic definitions and simple properties of threshold function are developed followed by a complete treatment of monotonicity theory algorithms for positivization and ordering of arguments are given both geometric and algebraic Heuristic Chow parameter and linear program methods of test synthesis are presented along with discussions of reduction theory and partially specified functions A geometric synthesis method for networks of 3 input majority gates is given and the author's method of general network synthesis discussed geometrically Two basic tables are included Although only the more basic concepts have been treated a

complete bibliography is provided together with suggestions for further reading Author *NeuralSource* Philip D. Wasserman,Roberta M. Oetzel,1990 Derived from the database Neural Base still available at 495 00 this bibliography covering more than 4 000 references is an important collection of research information Extensive annotations have been added to approximately 75% of the entries in the print version Periodicals private reports and books are included Indexed by author keyword and publication Neurons were slacking off when A mathematical theory was indexed under A Annotation copyrighted by Book News Inc Portland OR **Threshold Logic** Michael L. Dertouzos,1965 **Synthesis of Threshold Logic Networks** Stanford University. Stanford Electronics Laboratories,John E. Hopcroft,1964 The purpose of the research described in this report is to develop an algorithm for synthesizing Boolean switching functions with a minimum number of threshold logic units and to apply the algorithm and its supporting theory to the design of switching networks including multipleoutput networks The techniques previously available for the logical design of a switching function using threshold logic required considerable insight and did not in general yield economical networks This research develops an original synthesis procedure for designing a threshold logic network to realize an arbitrary switching function with a minimum number of threshold logic units An algorithm is developed that converts an originally nonlinearly separable problem into a number of linearly separable problems by specifying the intermediate outputs in a threshold logic network These linearly separable problems are then solved by already existing techniques such as linear programming The synthesis procedure is extended to cover the more general synthesis problem of realizing two or more switching functions with a multiple output network having the fewest number of threshold logic units Some of the techniques developed in this research are applicable to the field of adaptive systems In particular a procedure is given for adapting the weights of a threshold logic unit that will terminate for both linearly separable and nonlinearly separable problems Author Computer Literature Bibliography: 1964-1967 W. W. Youden,1965 *Adaptive Threshold Logic* Stanford University. Stanford Electronics Laboratories,1963 *Fuzzy Logic and Neural Network Handbook* Chi-hau Chen,1996 A practical reference that presents concise and comprehensive reports on the major activities in fuzzy logic and neural networks with emphasis on the applications and systems of interest to computer engineers Each of the 31 chapters focuses on the most important activity of a specific topic and the chapters are organized into three parts principles and algorithms applications and architectures and systems The applications for fuzzy logic include home appliance design and manufacturing process those for neural networks include radar sonar and speech signal processing remote sensing and electrical power systems Annotation copyright by Book News Inc Portland OR U.S. Government Research & Development Reports ,1970 **Multi-threshold Threshold Logic** Yao-tung Yen,1966

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