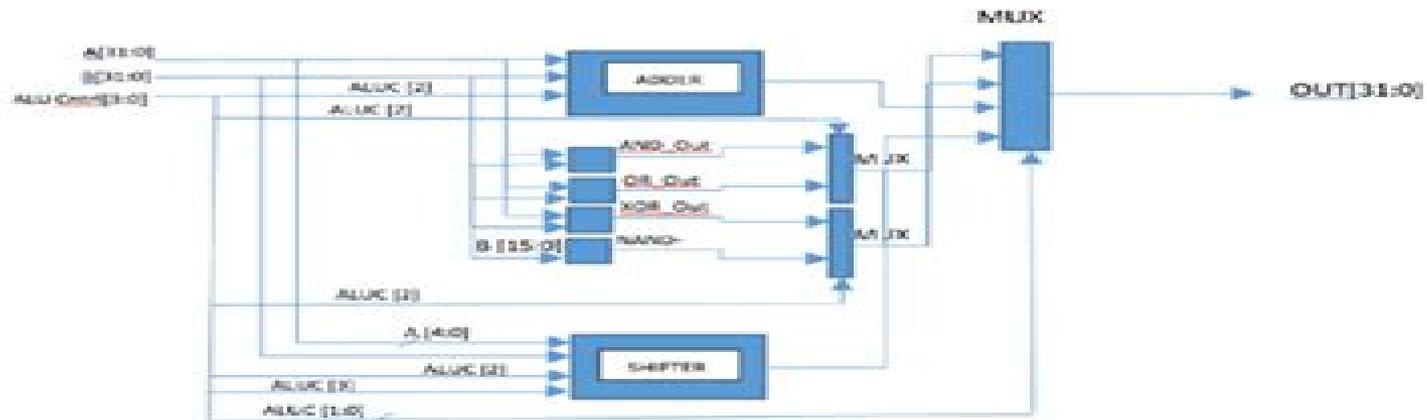# VLSI Architectures

Assignment 1 - Design of ALU

Yeshvanth M, 2023HT01517

High-level design in question

Solution

AND Gate

```
///////////////////////////////////////////////////////////////////////////////
/* 32-bit AND Gate used in ALU */
module AND
(
    input[31:0]   A_i,    // Input 1
    input[31:0]   B_i,    // Input 2
    output[31:0]  AND_o   // AND result
);

assign AND_o = A_i & B_i; // Behavioral modelling of AND gate

endmodule
```

Vlsi Algorithms And Architectures

Krzysztof Wojciech Przytula

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Design of VLSI Algorithms and Architectures for Digital Image and Signal Processing Krzysztof Wojciech Przytula,1985   **Algorithms for VLSI Physical Design Automation** Naveed A. Sherwani,2012-12-06 Algorithms for VLSI Physical Design Automation Second Edition is a core reference text for graduate students and CAD professionals Based on the very successful First Edition it provides a comprehensive treatment of the principles and algorithms of VLSI physical design presenting the concepts and algorithms in an intuitive manner Each chapter contains 3 4 algorithms that are discussed in detail Additional algorithms are presented in a somewhat shorter format References to advanced algorithms are presented at the end of each chapter Algorithms for VLSI Physical Design Automation covers all aspects of physical design In 1992 when the First Edition was published the largest available microprocessor had one million transistors and was

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disciplines and in the integrated circuit industry itself Within the last few years a community of researchers is growing to address issues closer to computer science the relationship between computing structures and the physical structures that implement them the specification and verification of computational procosses implemented in VLSI the use of massively parallel computing made possible by VLSI the design of special purpose computing architectures and the changes in general purpose computer architecture that VLSI makes possible It is likely that the future exploitation of VLSI technology depends as much on structural and design innovations as on advances in fabrication technology The book is divided into nine sections Invited Papers Six distinguished researchers from industry and academia presented invited papers Models of Computation The papers in this section deal with abstracting the properties of VLSI circuits into models that can be used to analyze the chip area time or energy required for a particular computation   Algorithms Vlsi Design Automation Gerez,2006-06 Market_Desc Electrical Engineering Students taking courses on VLSI systems CAD tools for VLSI Design Automation at Final Year or Graduate Level Computer Science courses on the same topics at a similar level Practicing Engineers wishing to learn the state of the art in VLSI Design Automation Designers of CAD tools for chip design in software houses or large electronics companies Special Features Probably the first book on Design Automation for VLSI Systems which covers all stages of design from layout synthesis through logic synthesis to high level synthesis Clear precise presentation of examples well illustrated with over 200 figures Focus on algorithms for VLSI design tools means it will appeal to some Computer Science as well as Electrical Engineering departments About The Book Enrollments in VLSI design automation courses are not large but it s a very popular elective especially for those seeking a career in the microelectronics industry Already the reviewers seem very enthusiastic about the coverage of the book being a better match for their courses than available competitors because it covers all design phases It has plenty of worked problems and a large no of illustrations It s a good list builder title that matches our strategy of focusing on topics that lie on the interface between Elec Eng and Computer Science

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