

Using
WAVES and **VHDL**
for
Effective Design and Testing



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2 Concept Tools Specification Tools Design Stages Tools Implementation Tools Figure 1 1

A nominal multi stage development process From that beginning we have progressed to the point where the EDA community at large including both users and developers of the tools are interested in more unified environments Here the notion is that the tools used at the various stages in the development process need to be able to complement each other and to communicate with one another efficiently using effective file exchange capabilities Furthermore the idea of capturing all the tool support needed for an EDA development into a unified support environment is now becoming a reality This reality is evidenced by some of the EDA suites we now see emerging wherein several tool functions are integrated under a common graphical user interface GUI with supporting file exchange and libraries to enable all tool functions to operate effectively and synergistically This concept which we illustrate in Figure 1 2 is the true future of EDA

Using WAVES and VHDL for Effective Design and Testing James P. Hanna, 1997

A standard VHDL model test and verification methodology facilitating automatic test bench generation as well as model and testbench interchange The volume beings in the waveform context familiar to all engineers and applies the WAVES principles and constructs in a sequential tutorial allowing the reader to build on knowledge and information and apply concepts

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Essentials of Electronic Testing for Digital, Memory and Mixed-Signal VLSI Circuits M. Bushnell, Vishwani Agrawal, 2004-12-15

The modern electronic testing has a forty year history Test professionals hold some fairly large conferences and numerous workshops have a journal and there are over one hundred books on testing Still a full course on testing is offered only at a few universities mostly by professors who have a research interest in this area Apparently most professors would not have taken a course on electronic testing when they were students Other than the computer engineering curriculum being too crowded the major reason cited for the absence of a course on electronic testing is the lack of a suitable textbook For VLSI the foundation was provided by semiconductor device technology circuit design and electronic testing In a computer engineering curriculum therefore it is necessary that foundations should be taught before applications The field of VLSI has expanded to systems on a chip which include digital memory and mixed signalsubsystems To our knowledge this is the first textbook to cover all three types of electronic circuits We have written this textbook for an undergraduate foundations course on electronic testing Obviously it is too voluminous for a one semester course and a teacher will have to select from the topics We did not restrict such freedom because the selection may depend upon the individual expertise and interests Besides there is merit in having a larger book that will retain its usefulness for the owner even after the completion of the course With equal tenacity we address the needs of three other groups of readers

Scientific and Technical Aerospace Reports, 1995

Proceedings, 11th IEEE International Conference and Workshop on the Engineering of

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