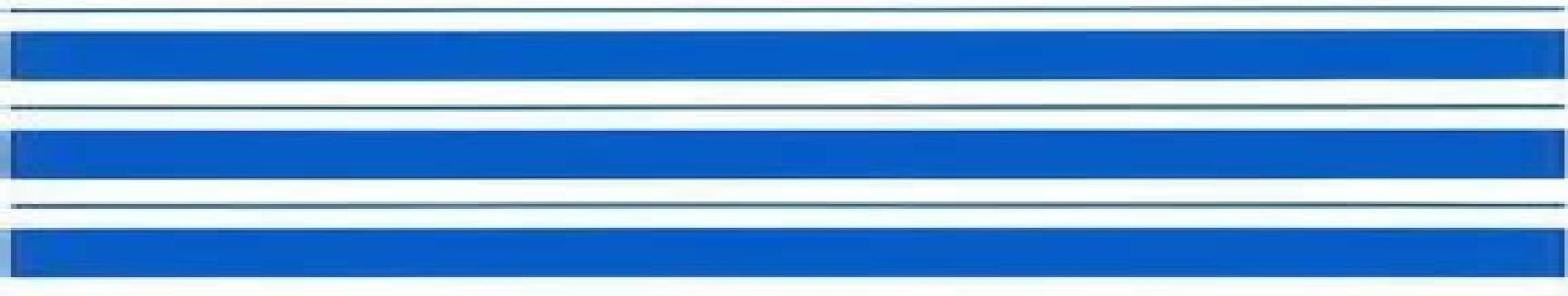


Unified Methods for VLSI Simulation and Test Generation

**Kwang-Ting Cheng
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