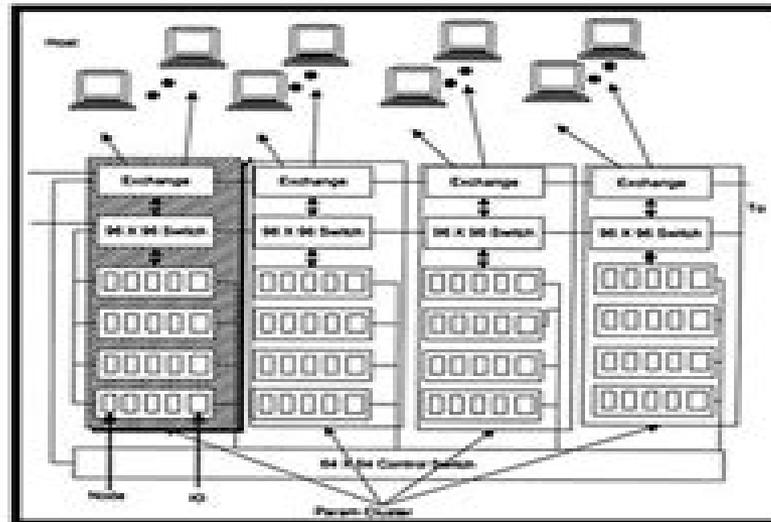


Still Image Compression on Parallel Computer Architectures

Savitri Bevinakoppa



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Still Image Compression on Parallel Computer Architectures Savitri Bevinakoppa, 1998-11-30 Still Image Compression on Parallel Computer Architectures investigates the application of parallel processing techniques to digital image compression. Digital image compression is used to reduce the number of bits required to store an image in computer memory and or transmit it over a communication link. Over the past decade advancements in technology have spawned many applications of digital imaging such as photo videotex desktop publishing graphics arts color facsimile newspaper wire phototransmission and medical imaging. For many other contemporary applications such as distributed multimedia systems rapid transmission of images is necessary. Dollar cost as well as time cost of transmission and storage tend to be directly proportional to the volume of data. Therefore application of digital image compression techniques becomes necessary to minimize costs. A number of digital image compression algorithms have been developed and standardized. With the success of these algorithms research effort is now directed towards improving implementation techniques. The Joint Photographic Experts Group JPEG and Motion Photographic Experts Group MPEG are international organizations which have developed digital image compression standards. Hardware VLSI chips which implement the JPEG image compression algorithm are available. Such hardware is specific to image compression only and cannot be used for other image processing applications. A flexible means of implementing digital image compression algorithms is still required. An obvious method of processing different imaging applications on general purpose hardware platforms is to develop software implementations. JPEG uses an 8 x 8 block of image samples as the basic element for compression. These blocks are processed sequentially. There is always the possibility of having similar blocks in a given image. If similar blocks in an image are located then repeated compression of these blocks is not necessary. By locating similar blocks in the image the speed of compression can be increased and the size of the compressed image can be reduced. Based on this concept an enhancement to the JPEG algorithm is proposed called Block Comparator Technique. BCT Still Image Compression on Parallel Computer Architectures is designed for advanced students and practitioners of computer science. This comprehensive reference provides a foundation for understanding digital image compression techniques and parallel computer architectures.

Digital Image Compression on Parallel Computer Architectures Savitridevi G. Bevinakoppa, 1996

Signal Processing for Image Enhancement and Multimedia Processing Ernesto Damiani, Albert Dipanda, Kokou Yetongnon, Louis Legrand, Peter Schelkens, Richard Chbeir, 2007-12-03 Traditionally signal processing techniques lay at the foundation of multimedia data processing and analysis. In the past few years a new wave of advanced signal processing techniques has delivered exciting results increasing systems capabilities of efficiently exchanging image data and extracting useful knowledge from them. *Signal Processing for Image Enhancement and Multimedia Processing* is an edited volume written by well recognized international researchers with extended chapter style versions of the best papers presented at the SITIS 2006 International Conference. This book presents the state of the art and recent research.

results on the application of advanced signal processing techniques for improving the value of image and video data It also discusses feature based techniques for deep feature oriented analysis of images and new results on video coding on the honored topic of securing image information Signal Processing for Image Enhancement and Multimedia Processing is designed for a professional audience composed of practitioners and researchers in industry This volume is also suitable as a reference or secondary text for advanced level students in computer science and engineering The chapters included in this book are a selection of papers presented at the Signal and Image Technologies track of the international SITIS 2006 conference The authors were asked to revise and extend their contributions to take into account the many challenges and remarks discussed at the conference A large number of high quality papers were submitted to SITIS 2006 demonstrating the growing interest of the research community for image and multimedia processing

Computer Architecture '98 John Morris, 1998-06-01 The third in the series of the Australasian Computer Architecture Conferences ACAC98 was held in Perth Australia on the 2nd and 3rd of February 1998 This year's submissions saw papers coming from Australia Austria China Germany Japan New Zealand Portugal Singapore Taiwan and the United States all presenting both novel as well as conventional architectural ideas The 20 papers presented in this volume also cover the vital area of connection between processors from geographically separated to closely coupled systems

Computed Synchronization for Multimedia Applications Charles B. Owen, Fillia Makedon, 2013-04-17 Multimedia is changing the design of database and information retrieval systems The accumulation of audio image and video content is of little use in these systems if the content cannot be retrieved on demand a critical requirement that has led to the development of new technologies for the analysis and indexing of media data In turn these technologies seek to derive information or features from a data type that can facilitate rapid retrieval efficient compression and logical presentation of the data Significant work that has not been addressed however is the benefits of analyzing more than one data type simultaneously *Computed Synchronization for Multimedia Applications* presents a new framework for the simultaneous analysis of multiple media data objects The primary benefit of this analysis is computed synchronization a temporal and spatial alignment of multiple media objects *Computed Synchronization for Multimedia Applications* also presents several specific applications and a general structure for the solution of computed synchronization problems The applications demonstrate the use of this structure Two applications in particular are described in detail the alignment of text to speech audio and the alignment of simultaneous English language translations of ancient texts Many additional applications are discussed as future uses of the technology *Computed Synchronization for Multimedia Applications* is useful to researchers students and developers seeking to apply computed synchronization in many fields It is also suitable as a reference for a graduate level course in multimedia data retrieval

Multimedia Mining Chabane Djeraba, 2002-11-30 *Multimedia Mining A Highway to Intelligent Multimedia Documents* brings together experts in digital media content analysis state of art data mining and knowledge discovery in multimedia database systems knowledge engineers and domain experts

from diverse applied disciplines Multimedia documents are ubiquitous and often required if not essential in many applications today This phenomenon has made multimedia documents widespread and extremely large There are tools for managing and searching within these collections but the need for tools to extract hidden useful knowledge embedded within multimedia objects is becoming pressing and central for many decision making applications The tools needed today are tools for discovering relationships between objects or segments within multimedia document components such as classifying images based on their content extracting patterns in sound categorizing speech and music and recognizing and tracking objects in video streams

Electronic Business and Education Wendy Chin, Frederic*accents Patricelli, V.

Milutinovic, 2012-12-06 In order to establish technical prerequisites for efficient electronic business and education on the Internet appropriate system support is needed as a vital condition for maximization of both short term and long term profits Electronic Business and Education Recent Advances in Internet Infrastructures discusses current research topics in the domain of system support for e business and e education on the Internet and stresses the synergistic interaction of these two components Attention is given to both scientific and engineering issues Electronic Business and Education Recent Advances in Internet Infrastructures is suitable as a secondary text for a graduate level course and as a reference for researchers and practitioners in industry particularly in the area of e business and e education on the Internet There is no longer any question that the Internet and electronic communication are the major new tools for collaborative advances in the creation of new knowledge and in future learning Excerpt from Foreword by Robert C Richardson Nobel Laureate 1996 Cornell University USA

Infrastructure for Electronic Business on the Internet Veljko Milutinovic, 2012-12-06 Design is an art form in which the designer selects from a myriad of alternatives to bring an optimum choice to a user In many complex of optimum is difficult to define Indeed the users systems the notion themselves will not agree so the best system is simply the one in which the designer and the user have a congruent viewpoint Compounding the design problem are tradeoffs that span a variety of technologies and user requirements The electronic business system is a classically complex system whose tradeoff criteria and user views are constantly changing with rapidly developing underlying technology Professor Milutinovic has chosen this area for his capstone contribution to the computer systems design This book completes his trilogy on design issue in computer systems His first work Surviving the Design of a 200 MHz RISC Microprocessor 1997 focused on the tradeoffs and design issues within a processor His second work Surviving the Design of Microprocessor and Multiprocessor Systems 2000 considers the design issues involved with assembling a number of processors into a coherent system Finally this book generalizes the system design problem to electronic commerce on the Internet a global system of immense consequence

Coding and Modulation for Digital Television Gordon M. Drury, Garik Markarian, Keith Pickavance, 2006-04-11

Coding and Modulation for Digital Television presents a comprehensive description of all error control coding and digital modulation techniques used in Digital Television DTV This book illustrates the relevant elements from the expansive theory

of channel coding to how the transmission environment dictates the choice of error control coding and digital modulation schemes. These elements are presented in such a way that both the mathematical integrity and understanding for engineers are combined in a complete form and supported by a number of practical examples. In addition, the book contains descriptions of the existing standards and provides a valuable source of corresponding references. Coding and Modulation for Digital Television also features a description of the latest techniques providing the reader with a glimpse of future digital broadcasting. These include the concepts of soft in soft out decoding, turbo coding, and cross-correlated quadrature modulation, all of which will have a prominent future in improving the efficiency of the next generation DTV systems. Coding and Modulation for Digital Television is essential reading for all undergraduate and postgraduate students, broadcasting and communication engineers, researchers, marketing managers, regulatory bodies, governmental organizations, and standardization institutions of the digital television industry. *CIC'2000* Brian J. D'Auriol, Abdullah Abonamah, 2000

The Impact of Computer Architecture Features on Image Processing Application Execution Times Jeremy Alan Salinger, 2000

CAMP '95, Computer Architectures for Machine Perception V. Cantoni, 1995. Proceedings of the September 1995 workshop on Computer Architectures for Machine Perception containing the results of sessions on smart sensors and artificial retinæ: three papers ASICs and VLSI architectures; five papers parallel architectures; three papers real time systems and techniques. *Sci-tech News*, 1999. *Book Review Index*, 2003. Vols 8-10 of the 1965-1984 master cumulation constitute a title index. *American Book Publishing Record*, 1999.

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Still Image Compression On Parallel Computer Architectures Book Review: Unveiling the Power of Words

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