

YIELD AND VARIABILITY OPTIMIZATION OF INTEGRATED CIRCUITS

J.C. Zhang
M.A. Styblinski



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Yield And Variability Optimization Of Integrated Circuits

KJ Lindholm-Leary



Yield And Variability Optimization Of Integrated Circuits:

Yield and Variability Optimization of Integrated Circuits Jian Cheng Zhang, M.A. Styblinski, 2013-03-09 Traditionally Computer Aided Design CAD tools have been used to create the nominal design of an integrated circuit IC such that the circuit nominal response meets the desired performance specifications In reality however due to the disturbances of the IC manufacturing process the actual performances of the mass produced chips are different than those for the nominal design Even if the manufacturing process were tightly controlled so that there were little variations across the chips manufactured the environmental changes e.g. those of temperature supply voltages etc would also make the circuit performances vary during the circuit life span Process related performance variations may lead to low manufacturing yield and unacceptable product quality For these reasons statistical circuit design techniques are required to design the circuit parameters taking the statistical process variations into account This book deals with some theoretical and practical aspects of IC statistical design and emphasizes how they differ from those for discrete circuits It describes a spectrum of different statistical design problems such as parametric yield optimization generalized on target design variability minimization performance tuning and worst case design The main emphasis of the presentation is placed on the principles and practical solutions for performance variability minimization It is hoped that the book may serve as an introductory reference material for various groups of IC designers and the methodologies described will help them enhance the circuit quality and manufacturability The book contains seven chapters

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Scientific and Technical Aerospace Reports, 1994-07 Computer-Aided Design of Analog Integrated Circuits and Systems Rob A. Rutenbar, Georges G. E. Gielen, 2002-05-06 The tools and techniques you need to break the analog design bottleneck Ten years ago analog seemed to be a dead end technology Today System on Chip SoC designs are increasingly mixed signal designs With the advent of application specific integrated circuits ASIC technologies that can integrate both analog and digital functions on a single chip analog has become more crucial than ever to the design process Today designers are moving beyond hand crafted one transistor at a time methods They are using new circuit and physical synthesis tools to design practical analog circuits new modeling and analysis tools to allow rapid exploration of system level alternatives and new simulation tools to provide accurate answers for analog circuit behaviors and interactions that were considered impossible to handle only a few years ago To give circuit designers and CAD professionals a better understanding of the history and the current state of the art in the field this volume collects in one place the essential set of analog CAD papers that form the foundation of today's new analog design automation tools Areas covered are Analog synthesis Symbolic analysis Analog layout Analog modeling and analysis Specialized analog simulation Circuit centering and yield optimization Circuit testing Computer Aided Design of Analog Integrated Circuits and Systems is the cutting edge reference that will be an invaluable resource for every semiconductor circuit designer and CAD professional who hopes to break the analog design

bottleneck *The Circuits and Filters Handbook* Wai-Kai Chen,2002-12-23 A bestseller in its first edition The Circuits and Filters Handbook has been thoroughly updated to provide the most current most comprehensive information available in both the classical and emerging fields of circuits and filters both analog and digital This edition contains 29 new chapters with significant additions in the areas of computer *Proceedings of the ... Midwest Symposium on Circuits and Systems* ,2003
1994 IEEE International Symposium on Circuits and Systems ,1994 **Computer Aided Design and Design Automation** Wai-Kai Chen,2018-03-12 This volume of The Circuits and Filters Handbook Third Edition focuses on computer aided design and design automation In the first part of the book international contributors address topics such as the modeling of circuit performances symbolic analysis methods numerical analysis methods design by optimization statistical design optimization and physical design automation In the second half of the text they turn their attention to RF CAD high performance simulation formal verification RTK behavioral synthesis system level design an Internet based micro electronic design automation framework performance modeling and embedded computing systems design Microelectronic Manufacturing Yield, Reliability, and Failure Analysis ,1997 **Design of High-Performance Microprocessor Circuits** Anantha Chandrakasan,2001 The authors present readers with a compelling one stop advanced system perspective on the intrinsic issues of digital system design This invaluable reference prepares readers to meet the emerging challenges of the device and circuit issues associated with deep submicron technology It incorporates future trends with practical contemporary methodologies **International Conference on Computer Design (ICCD '99)** IEEE Computer Society,1999 The proceedings from the October 1999 conference include 107 technical presentations from 14 different countries Not restricted to presented papers this volume includes both the keynote and plenary addresses poster presentations as well as the proceedings of two tutorials one on CAD and one on benchmarking selecting and debugging microcontrollers Topics covered include applied verification techniques computer arithmetic intelligent memory design convergence test generation and delay testing microarchitecture and digital signal processors No subject index Annotation copyrighted by Book News Inc Portland OR **Reliability, Testing, and Characterization of MEMS/MOEMS.** ,2001
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In a world defined by information and interconnectivity, the enchanting power of words has acquired unparalleled significance. Their ability to kindle emotions, provoke contemplation, and ignite transformative change is truly awe-inspiring. Enter the realm of "**Yield And Variability Optimization Of Integrated Circuits**," a mesmerizing literary masterpiece penned by way of a distinguished author, guiding readers on a profound journey to unravel the secrets and potential hidden within every word. In this critique, we shall delve into the book's central themes, examine its distinctive writing style, and assess its profound effect on the souls of its readers.

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