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regulate their output using the method called pulse width modulation pwm

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web the figure given below represents the functional block diagram of smps initially the unregulated ac input signal from the source is provided to the input rectifier and filter circuit here the ac input signal is rectified to generate a dc signal and further smoothed to remove high frequency noise component from it

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web smps regulates the output power by varying the on to off time using minimum voltage so that efficiency is very high compared to the linear power supply what is inside a smps fig 1 simple block diagram showing different function of smps

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